

4-bit Synchronous Counter (74LS161)

```
// 4-bit counter

module counter_4bit(clk, nclr, nload, ent, enp, rco, count, parallel_in) :

    input  clk ;
    input  nclr ;
    input  nload ;
    input  ent ;
    input  enp ;
    output rco ;
    output [3:0] count ;
    input  [3:0] parallel_in ;

    reg    [3:0] count ;
    reg    rco ;

    always @ (count or ent)
    if ((count == 15) && (ent == 1'b1))
        rco = 1'b1 ;
    else
        rco = 1'b0 ;

    always @ (posedge clk or negedge nclr)
    begin
        if (nclr == 1'b0)
            count <= 4'd0 ;
        else if (nload == 1'b0)
            count <= parallel_in ;
        else if ((ent == 1'b1) && (enp == 1'b1))
            count <= (count + 1) & 16 ;
    end
endmodule
```

4 Bit Counter Verilog Code Davefc

Liyong Dong



4 Bit Counter Verilog Code Davefc:

The Enigmatic Realm of **4 Bit Counter Verilog Code Davefc**: Unleashing the Language is Inner Magic

In a fast-paced digital era where connections and knowledge intertwine, the enigmatic realm of language reveals its inherent magic. Its capacity to stir emotions, ignite contemplation, and catalyze profound transformations is nothing in short supply of extraordinary. Within the captivating pages of **4 Bit Counter Verilog Code Davefc** a literary masterpiece penned by way of a renowned author, readers set about a transformative journey, unlocking the secrets and untapped potential embedded within each word. In this evaluation, we shall explore the book's core themes, assess its distinct writing style, and delve into its lasting effect on the hearts and minds of those that partake in its reading experience.

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