Tanner EDA Releases T-Spice

Pasadena, Calif.--Feb. 23, 1998-- Tanner EDA (Pasadena) announced the V5 release of the T-Spice Pro integrated circuit design capture and simulation system for Windows 95 and NT. The tool suite includes the S-Edit schematic editor, the T-Spice circuit simulator, and the W-Edit waveform viewer. The new release includes improvements in userinterface, integration, and simulation and modeling technology.

Tanner's hierarchic schematic capture tool, S-Edit, has been overhauled to add manynew Windows 95 and NT user interface improvements. Other enhancements include asuperior simulation interface and more flexible design export capabilities. A newbrowser gives quick access to cell library components. Library files can be easilyadded, removed, or accessed for design reuse. A "quick pick" feature gives fastaccess to frequently used components.

S-Edit is well integrated with the T-Spice circuit simulator and W-Edit waveformviewer. Simulation results can be viewed by probing the schematic using the newwaveform probing feature. New design export features include support forparametric subcircuits, control of subcircuit pin orders, and output in EDIF 2 0 0 andVHDL formats. Compatibility with L-Edit simplifies the design process--there is nolonger a need to use mismatched design capture, simulation, and layout tools.

Tanner's circuit simulator, T-Spice, contains the BSIM3v3.1 MOSFET model, thelatest version of Berkeley's BSIM3 model. New MESFET models have also beenadded for GaAs IC support. T-Spice accepts the latest parasitic and size-based devicecharacterization data available from foundries to give accurate simulation results.

For devices that cannot be represented by one of T-Spice's internal models, custommodels can be written using a new external model interface. Written in the Cprogramming language, external models can be simulated using a built inC-interpreter, or compiled into a dynamically linked library (DLL). The generalpurpose interface allows the addition of functional and behavioral level models inaddition to custom transistor models.

T-Spice's new enhanced Fourier analysis command contains new accuracy andoutput control options. A waveform relaxation engine has been added as analternative to the standard transient analysis method for simulation of large circuits.Simulation queueing is also supported for running multiple T-Spice analyses.Several new commands and options have been added to control simulation methods,output data compression, case sensitivity, device scaling, output expressionhandling, and simulation statistics. In addition, T-Spice's netlist manipulation toolshave been enhanced with undo and redo, and regular expression search and replacecapabilities.

Additions to W-Edit improve the power and flexibilty for graphically viewingsimulation output data. New controls provide precise adjustment of the graphicaldisplay of output data. A waveform arithmetic tool can display traces formed bywriting mathematical expressions. Traces defined by expressions can be composedof arithmetic combinations of other trace names or can

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Julia Schneider

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