

The top portion of the cover features a red background with white circuit diagrams. These diagrams include a complex network of lines at the top, followed by two distinct CMOS-like structures. Each structure consists of a PMOS transistor at the top and an NMOS transistor at the bottom, connected to a common node. The background also contains faint, larger-scale circuit patterns and arrows.

# CMOS Analog Design Using All-Region MOSFET Modeling

Márcio Cherem Schneider and  
Carlos Galup-Montoro

The bottom portion of the cover features a dark red background with white circuit diagrams. These diagrams are more complex and dense than those in the top section, showing various interconnections and components.

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# Cmos Analog Design Using All Region Mosfet Modeling

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