

Semiconductor Devices

Short Notes

PN Junction

P-type: When trivalent impurities are added or doped to pure (intrinsic) semiconductor, p-type semiconductor is formed.

Trivalent Impurities → Gallium, Indium, etc.

N-type: When pentavalent impurities are added or doped to pure semiconductor, n-type semiconductor is formed.

Pentavalent Impurities → Antimony, Arsenic

P-type
Holes (+ve charge)
Majority charge carriers
Electrons (-ve charge)
Minority charge carriers

N-type
Electrons
Majority charge carriers
Holes
Minority charge carriers

Do majority and minority charge carriers vary? \rightarrow 100% P-type or 100% N-type semiconductor contains some amount of electron in holes and N-type contains some amount of holes in electrons. So, P-type has electron as minority charge carriers and N-type has holes as minority charge carriers.



If P-type & N-type are attached, diffusion phenomena occurs. Charges get diffused or moved to another end, which creates a potential barrier or depletion layer.



PN Diode (Diode - Device)

Diode is a device made by use of semiconductor.



Fig: Diode



Fig: Symbolical representation of PN Junction diode

Forward Biasing of Diode



Fig: Forward Biasing. Positive charges have to get holes, recombination happens, the charge motion in depletion layer is not a flow of charge, electricity flows through the diode.



Fig: Reverse Biasing. No flow of electricity. Yes, there is a -ve to +ve charge attraction, but the side that accumulates positive charge is not a flow.

Forward Biasing Characteristics



Fig: Circuit diagram of Forward Biasing



Fig: Forward Characteristics

Series rheostat to help in voltage reduction, current control, yes, meaning potential barrier is potential barrier, voltage supply, hinders, A-hai voltage bad, potential barrier ko

potential barrier, bad voltage, slow, gradual increase of current, yes, at point where potential barrier sharply increases, yes, take matter, change, now depletion layer, barrier, smoothly flow.

Reverse Biasing Characteristics

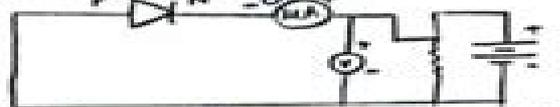


Fig: Circuit diagram of reverse biasing



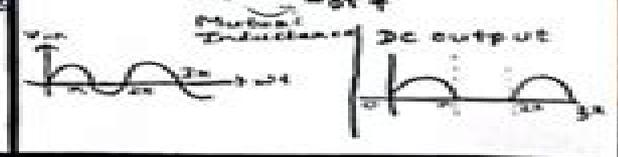
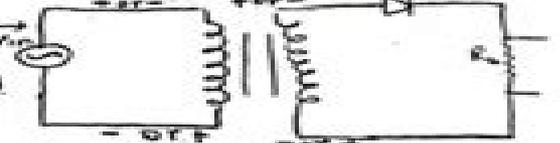
Fig: Reverse Characteristics

Semiconductor is pure state, but contains impurities. P-type contains thermal electrons, holes, n-type contains thermal electrons, holes as minority charge carriers. So -ve charge carriers flow, holes as minority charge carriers. Some concept again, yes.

Yes, breakdown voltage, but current sharply increases.

Rectification: Process of converting AC to DC

1. Half-wave Rectification



Semiconductor 12th Class Chapter Notes

BM King



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