

NX20P5090

High-voltage USB-PC power switch

Rev. 1 — 04 October 2018

Product data sheet

1. General description

The NX20P5090 is an advanced 5 A synchronous power switch for USB PD. It includes under-voltage lockout, over-voltage lockout, reverse-current protection and over-temperature protection circuits. It is designed to automatically lock the power path terminals when a fault condition occurs. Both VBUS and GND pins have PD 4 compliance in shutdown mode. Two NX20P5090 chips can be used in parallel to support dual power inputs connecting to the same charging port.

The device has a default 30 V over-voltage protection threshold and the OVP threshold can be adjusted by using an external resistor divider on DVDD pin. It is made to ensure that it is triggered every time before the device is switched ON. Alternatively, it can also be used by using the default value.

Designed for operation from 0.5 V to 5.5 V, used in USB PD power-control applications to offer excellent protection and enhance system reliability.

NX20P5090 is offered in a small 10 pins, 2.00 x 1.20 x 0.50 mm WLCSP package.

2. Features and benefits

- Wide supply voltage range from 0.5 V to 5.5 V
- Low resistance in a continuous current
- PD 4 compliance on both VBUS and GND pins
- 30 V OVP (adjustable) with OVP protection
- Adjustable VBUS over-voltage protection
- Built-in over-temperature lock (power) current limit
- 40 times fast fault response-time protection
- Protection modes
 - Over-Temperature Protection
 - Over-voltage Protection
 - Under-voltage lockout
 - Reverse Current Protection
- Surge protection
 - NX20P5090-A is provided with 100 V VBUS reverse impedance
 - NX20P5090-B is provided with 100 V VBUS with 20 VV impedance

The NXP logo consists of the letters 'N', 'X', and 'P' in a stylized, colorful font. The 'N' is orange, the 'X' is blue, and the 'P' is green. They are arranged horizontally and slightly overlap.

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Effective Action Calculation in Lattice QCD Jaap Hoek, 1983

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