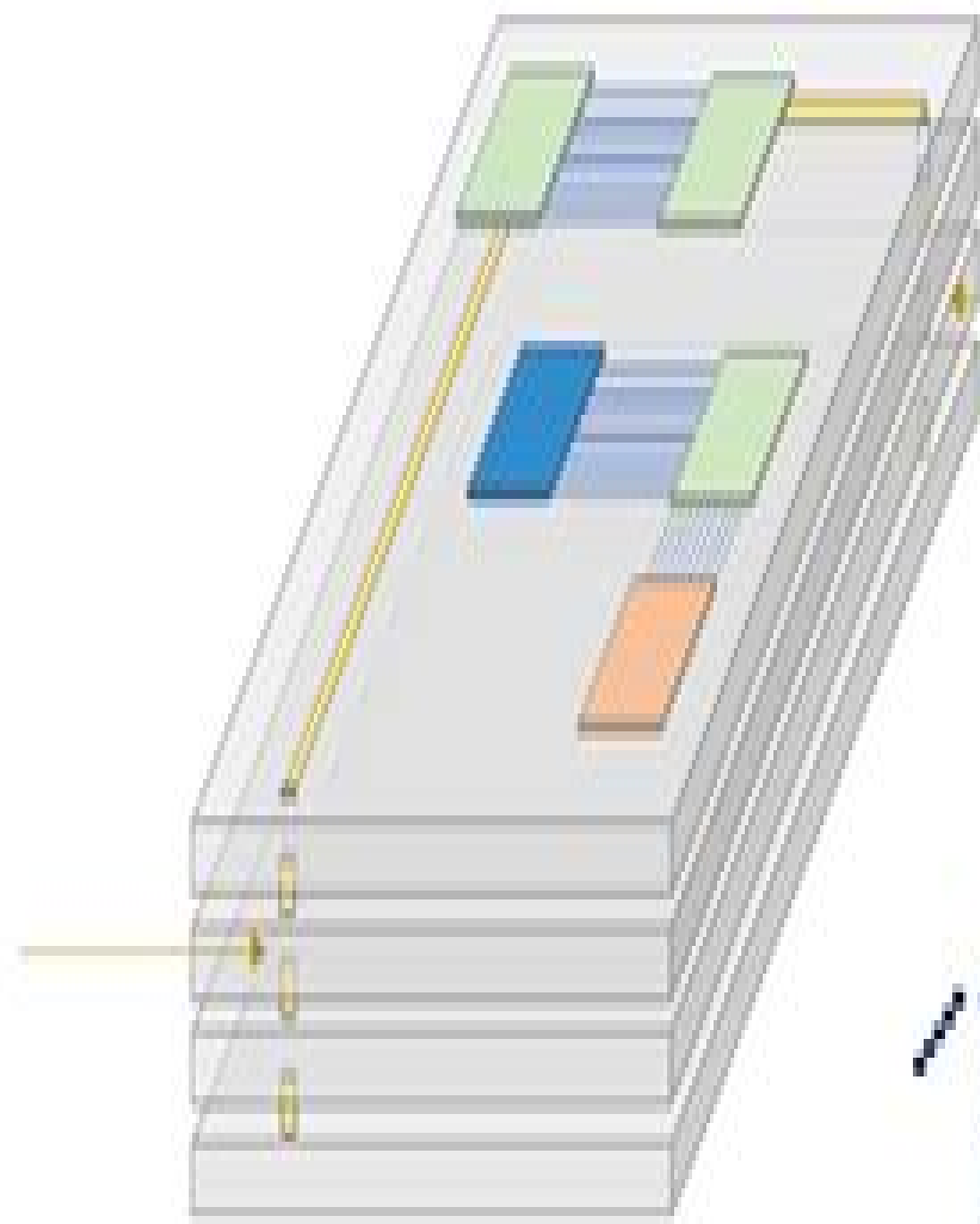


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Modeling and Design of Electromagnetic Compatibility for High-Speed Printed Circuit Boards and Packaging Xing-Chang Wei, 2017-09-19 Modeling and Design of Electromagnetic Compatibility for High Speed Printed Circuit Boards and Packaging presents the electromagnetic modelling and design of three major electromagnetic compatibility EMC issues related to the high speed printed circuit board PCB and electronic packages signal integrity SI power integrity PI and electromagnetic interference EMI The emphasis is put on two essential passive components of PCBs and packages the power distribution network and the signal distribution network This book includes two parts Part one talks about the field circuit hybrid methods used for the EMC modeling including the modal method the integral equation method the cylindrical wave expansion method and the de embedding method Part two illustrates EMC design methods and explores the applications of novel metamaterials and two dimensional materials on traditional EMC problems This book is designed to enhance worthwhile electromagnetic theory and mathematical methods for practical engineers and to train students with advanced EMC applications

TSV 3D RF Integration Shenglin Ma, Yufeng Jin, 2022-04-27 TSV 3D RF Integration High Resistivity Si Interposer Technology systematically introduces the design process development and application verification of high resistivity silicon interpose technology addressing issues of high frequency loss and high integration level The book includes a detailed demonstration of the design and process development of Hr Si interposer technology gives case studies and presents a systematic literature review Users will find this to be a resource with detailed demonstrations of the design and process development of HR Si interposer technologies including quality monitoring and methods to extract S parameters A series of cases are presented including an example of an integrated inductor a microstrip inter digital filter and a stacked patch antenna Each chapter includes a systematic and comparative review of the research literature offering researchers and engineers in microelectronics a uniquely useful handbook to help solve problems in 3D heterogenous RF integration oriented Hr Si interposer technology Provides a detailed demonstration of the design and process development of HR Si High Resistivity Silicon interposer technology Presents a series of implementation case studies that detail modeling and simulation integration qualification and testing methods Offers a systematic and comparative literature review of HR Si interposer technology by topic Offers solutions to problems with TSV through silicon via interposer technology including high frequency

loss and cooling problems Gives a systematic and accessible accounting on this leading technology *Issues in Electronic Circuits, Devices, and Materials: 2013 Edition*, 2013-05-01 *Issues in Electronic Circuits Devices and Materials 2013 Edition* is a ScholarlyEditions book that delivers timely authoritative and comprehensive information about Microwave Research The editors have built *Issues in Electronic Circuits Devices and Materials 2013 Edition* on the vast information databases of ScholarlyNews You can expect the information about Microwave Research in this book to be deeper than what you can access anywhere else as well as consistently reliable authoritative informed and relevant The content of *Issues in Electronic Circuits Devices and Materials 2013 Edition* has been produced by the world's leading scientists engineers analysts research institutions and companies All of the content is from peer reviewed sources and all of it is written assembled and edited by the editors at ScholarlyEditions and available exclusively from us You now have a source you can cite with authority confidence and credibility More information is available at <http://www.ScholarlyEditions.com> **Analyse Et Caractérisation**

Des Couplages Substrat Et de la Connectique Dans Les Circuits 3D Fengyuan Sun, 2016 The proposal of doubling the number of transistors on an IC chip with minimum costs and subtle innovations every 24 months by Gordon Moore in 1965 the so called Moore's law has been the most powerful driver for the emphasis of the microelectronics industry in the past 50 years This law enhances lithography scaling and integration in 2D of all functions on a single chip increasingly through system on chip SOC On the other hand the integration of all these functions can be achieved through 3D integrations Generally speaking 3D integration consists of 3D IC packaging 3D IC integration and 3D Si integration They are different and mostly the TSV through silicon via separates 3D IC packaging from 3D IC Si integrations since the latter two uses TSVs but 3D IC packaging does not TSV with a new concept that every chip or interposer could have two surfaces with circuits is the heart of 3D IC Si integrations Continued technology scaling together with the integration of disparate technologies in a single chip means that device performance continues to outstrip interconnect and packaging capabilities and hence there exist many difficult engineering challenges most notably in power management noise isolation and intra and inter chip communication 3D Si integration is the right way to go and compete with Moore's law more than Moore versus more Moore However it is still a long way to go In this book Fengyuan SUN proposes new substrate network extraction techniques Using this latter the substrate coupling and loss in IC's can be analyzed He implements some Green TLM Transmission Line Matrix algorithms in MATLAB It permits to extract impedances between any number of embedded contacts or and TSVs He does investigate models of high aspect ratio TSV on both analytical and numerical methods electromagnetic simulations This model enables to extract substrate and TSV impedance S parameters and parasitic elements considering the variable resistivity of the substrate It is full compatible with SPICE like solvers and should allow an investigation in depth of TSV impact on circuit performance **Science Abstracts**, 1995 **Applied Mechanics Reviews**, 1992 *Data Sources*, 2000 **Computer & Control Abstracts**, 1996 [Electrical & Electronics Abstracts](#), 1997 *Intervention '87*, 1987

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