

HND Level 100 - Computer Architecture

Structured Questions and Answers

Section A: Internal Structure and Operation of a Microprocessor (20 Marks)

1. a. Define the term microprocessor and list two of its major functions. (4 marks)

Answer:

A microprocessor is an integrated circuit that performs arithmetic, logic, control, and input/output operations of a computer system. It acts as the brain of a computer.

Functions:

- Performs arithmetic and logical operations
- Controls other components of the computer system

b. Describe the internal structure of a basic microprocessor with a labeled diagram. (6 marks)

Answer:

The internal structure typically includes:

- Arithmetic Logic Unit (ALU)
- Control Unit
- Registers
- Buses (data, address, control)

[Include labeled diagram manually in class or on whiteboard]

c. Explain the function of the following microprocessor components: (6 marks)

i. ALU

Performs arithmetic and logic operations.

ii. Control Unit

Directs the operations of the processor.

iii. Registers

Temporary storage locations for instructions and data.

d. Outline the fetch-decode-execute cycle. (4 marks)

Answer:

- Fetch: Retrieve instruction from memory.
- Decode: Interpret the instruction.
- Execute: Carry out the instruction.

Section B: Computer Memory (20 Marks)

2. a. Define computer memory and distinguish between primary and secondary memory. (4 marks)

Computer Architecture Questions And Answers Objectives

Mocktime Publication



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Advanced Data Mining and Applications Bohan Li,Lin Yue,Jing Jiang,Weitong Chen,Xue Li,Guodong Long,Fei Fang,Han Yu,2022-01-31 This book constitutes the proceedings of the 17th International Conference on Advanced Data Mining and Applications ADMA 2021 held in Sydney Australia in February 2022 The 26 full papers presented together with 35 short papers were carefully reviewed and selected from 116 submissions The papers were organized in topical sections in Part I including Healthcare Education Web Application and On device application The conference was originally planned for December 2021 but was postponed to 2022

Fifth Generation Computer Systems T. Moto-Oka,2012-12-02 The Japan Information Processing Development Centre JIPDEC established a committee for Study and Research on Fifth Generation Computers Beginning in 1979 this Committee set out on a two year investigation into the most desirable types of computer systems for application in the 1990 s fifth generation computers and how the development

projects aimed at the realization of these systems should be carried forward This book contains the papers presented at the International Conference on Fifth Generation Computer Systems Included among these papers is a preliminary report on the findings of the Committee

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Fifth Generation Computer Systems Tohru Moto-oka,1982 Keynote speech Overview report Knowledge information processing research plan Architecture research plan Invicted lectures knowledge information processing Invicteds lectures architecture Major contributions of research and planning committee of fifth generation computer systems 1979 1981 Autor index

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The No-nonsense Guide to Computing Careers Marc Rettig,1992

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