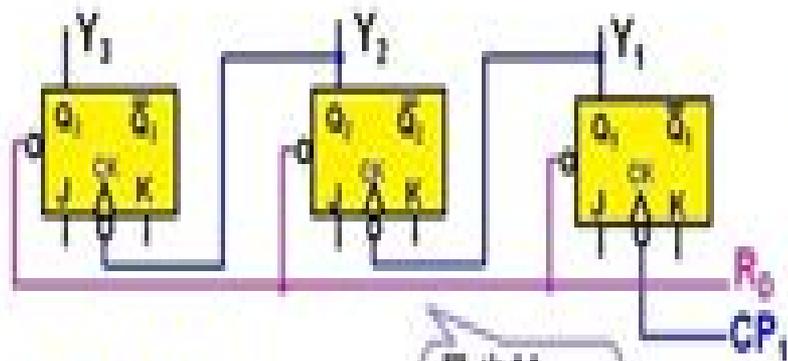


Digital Logic Design - Asynchronous Counter

③ 状态转换表



① 输入方程

$$J_1 = K_1 = 1 \quad CP_1 \downarrow$$

$$J_2 = K_2 = 1 \quad CP_2 = Y_1 \downarrow$$

$$J_3 = K_3 = 1 \quad CP_3 = Y_2 \downarrow$$

异步Moor
型时序电路

异步模8加
法计数器

现态			次态			时钟		
Y_3^n	Y_2^n	Y_1^n	Y_3^{n+1}	Y_2^{n+1}	Y_1^{n+1}	CP_3	CP_2	CP_1
0	0	0	0	0	1	无	无	↓
0	0	1	0	1	0	无	↓	↓
0	1	0	0	1	1	无	无	↓
0	1	1	1	0	0	↓	↓	↓
1	0	0	1	0	1	无	无	↓
1	0	1	1	1	0	无	↓	↓
1	1	0	1	1	1	无	无	↓
1	1	1	0	0	0	↓	↓	↓

② 次态方程

$$Y_1^{n+1} = J_1 \bar{Q}_1 + \bar{K}_1 Q_1 = \bar{Y}_1 \quad CP_1 \downarrow$$

$$Y_2^{n+1} = J_2 \bar{Q}_2 + \bar{K}_2 Q_2 = \bar{Y}_2 \quad Y_1 \downarrow$$

$$Y_3^{n+1} = J_3 \bar{Q}_3 + \bar{K}_3 Q_3 = \bar{Y}_3 \quad Y_2 \downarrow$$

④ 波形图

二分频



Digital Logic Design Solution

Albert A Gayle



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Solutions manual Gideon Langholz, Abraham Kandel, Joe L. Mott, 1988 Introduction to Digital Logic Design John Patrick Hayes, 1993-01-01 **Solutions Manual for Digital Logic and State Machine Design** David J. Comer, 1984

Solutions Manual to Digital Logic and State Machine Design David J. Comer, 1995 This book is intended for undergraduate students in Electrical Engineering *Digital Logic Testing and Simulation* Alexander Miczo, 1985-12 The new standard in the field presenting the latest design and testing methods for logic circuits and the development of a BASIC based simulation Offers designers and test engineers unique coverage of circuit design for testability stressing the incorporation of hardware into designs that facilitate testing and diagnosis by allowing greater access to internal circuits Examines various ways of representing a design as well as external testing methods that apply this information The Logic Design of Transistor Digital Computers Gerald A. Maley, John Earle, 1963 **Introduction to Logic Design** Sajjan G. Shiva, 2018-10-03 The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic instead of electronics level It covers a range of topics from number system theory to asynchronous logic design A solution manual is available to instructors only Requests must be made on official school stationery **Solutions Manual to Accompany Digital Logic Testing and Simulation** Alexander Miczo, 1986 *Solutions Manual to Accompany Digital Logic Applications and Design*, John M. Yarbrough Bassam H. Matar, 1998 *Transforming Tomorrow: Innovative Solutions and Global Trends in Electrical and Electronics Engineering* Namit Gupta, Anjali Gupta, Naresh Purohit, Shiraz Husain, 2025-06-03 The International Conference on Transforming Tomorrow Innovative Solutions and Global Trends in Electrical and Electronics Engineering Pragyata 2025 is scheduled to be held on May 5 6 2025 at Shri Vaishnav Vidyapeeth Vishwavidyalaya Indore Madhya Pradesh India This prestigious event aims to provide a dynamic platform for researchers academicians industry professionals and students to exchange knowledge showcase cutting edge innovations and discuss global trends shaping the future of Electrical and Electronics Engineering Pragyata 2025 will feature sessions and presentations on key emerging areas including Robotics Renewable Energy Smart Grids Mechatronics 5G Communications Artificial Intelligence and the Internet of Things IoT The conference is designed to foster meaningful dialogue cross disciplinary collaboration and engagement with leading experts from academia and industry In line with its theme of Transforming Tomorrow the conference emphasizes clarity innovation and sustainable development It will serve as a catalyst for forward looking discussions and solutions that address modern engineering challenges and contribute to building a smarter greener and more connected world With a commitment to being Concise Clear and Cohesive Pragyata 2025 is set to become a significant academic and professional milestone in advancing technological progress and inspiring future innovation across the Electrical and Electronics Engineering spectrum Instructor's Solutions Manual to Accompany Fundamentals of Digital Logic with Vhdl Design Lord Brown, Stephen D. Brown, Zvonko G. Vranesic, 1999-10-01 *Basic*

VLSI Design Technology Cherry Bhargava, Gaurav Mani Khanal, 2022-09-01 The current cutting edge VLSI circuit design technologies provide end users with many applications increased processing power and improved cost effectiveness This trend is accelerating with significant implications on future VLSI and systems design VLSI design engineers are always in demand for front end and back end design applications The book aims to give future and current VLSI design engineers a robust understanding of the underlying principles of the subject It not only focuses on circuit design processes obeying VLSI rules but also on technological aspects of fabrication The Hardware Description Language HDL Verilog is explained along with its modelling style The book also covers CMOS design from the digital systems level to the circuit level The book clearly explains fundamental principles and is a guide to good design practices The book is intended as a reference book for senior undergraduate first year post graduate students researchers as well as academicians in VLSI design electronics electrical engineering and materials science The basics and applications of VLSI design from digital system design to IC fabrication and FPGA Prototyping are each covered in a comprehensive manner At the end of each unit is a section with technical questions including solutions which will serve as an excellent teaching aid to all readers Technical topics discussed in the book include Digital System Design Design flow for IC fabrication and FPGA based prototyping Verilog HDL IC Fabrication Technology CMOS VLSI Design Miscellaneous It covers basics of Electronics and Reconfigurable computing PLDs Latest technology etc

Sensors and Microsystems Piero Malcovati, Andrea Baschiroto, Arnaldo d'Amico, Corrado Natale Di, 2010-03-14 Sensors and Microsystems contains a selection of papers presented at the 14th Italian conference on sensors and microsystems It provides a unique perspective on the research and development of sensors microsystems and related technologies in Italy The scientific values of the papers also offers an invaluable source to analysts intending to survey the Italian situation about sensors and microsystems In an interdisciplinary approach many aspects of the disciplines are covered ranging from materials science chemistry applied physics electronic engineering and biotechnologies Further details of the conference and its full program at the website <http://www.microelectronicsevents.com> AISEM

Soft Computing in Engineering Design and Manufacturing Pravir K. Chawdhry, Rajkumar Roy, Raj K. Pant, 2012-12-06 Soft Computing has emerged as an important approach towards achieving intelligent computational paradigms where key elements are learning from experience in the presence of uncertainties fuzzy belief functions and evolution of the computing strategies of the learning agent itself Fuzzy neural and evolutionary computing are the three major themes of soft computing The book presents original research papers dealing with the theory of soft computing and its applications in engineering design and manufacturing The methodologies have been applied to a large variety of real life problems Application of soft computing has provided the opportunity to integrate human like vagueness and real life uncertainty to an otherwise hard computer programme Now a computer programme can learn adapt and evolve using soft computing The book identifies the strengths and limitations of soft computing techniques particularly with reference to their engineering applications The applications

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