The Software Optimization Cookbook

High-Performance Recipes for IA-32 Platforms

Richard Gerber, Aart J.C. Bik, Kevin B. Smith, and Xinmin Tian

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The Software Optimization Cookbook ,2006 *Introduction to High Performance Computing for Scientists and Engineers* Georg Hager,2010-07-02 Written by high performance computing HPC experts Introduction to High Performance Computing for Scientists and Engineers provides a solid introduction to current mainstream computer architecture dominant parallel programming models and useful optimization strategies for scientific HPC From working in a scientific computing center the author *Parallel Programming with Intel Parallel Studio XE* Stephen Blair-Chappell,Andrew Stokes,2012-04-19 Optimize code for multi core processors with Intel s Parallel Studio Parallel programming is rapidly becoming a must know skill for developers Yet where to start This teach yourself tutorial is an ideal starting point for developers who already know Windows C and C and are eager to add parallelism to their code With a focus on applying tools techniques and language extensions to implement parallelism this essential resource teaches you how to write programs for multicore and leverage the power of multicore in your programs Sharing hands on case studies and real world examples the authors examine the challenges of each project and show you how to overcome them Explores conversion of serial code to parallel Focuses on implementing Intel Parallel Studio Highlights the benefits of using parallel code Addresses error and performance optimization of code Includes real world scenarios that illustrate the techniques of advanced parallel programming situations Parallel Programming with Intel Parallel Studio dispels any concerns of difficulty and gets you started creating faster code with Intel Parallel Studio *Embedded System Design* Peter Marwedel,2010-11-16 Until the late 1980s information processing was associated with large mainframe computers and huge tape drives During the 1990s this trend shifted toward information processing with personal computers or PCs The trend toward miniaturization continues and in the future the majority of information processing systems will be small mobile computers many of which will be embedded into larger products and interfaced to the physical environment Hence these kinds of systems are called embedded systems Embedded systems together with their physical environment are called cyber physical systems Examples include systems such as transportation and fabrication equipment It is expected that the total market volume of embedded systems will be significantly larger than that of traditional information processing systems such as PCs and mainframes Embedded systems share a number of common characteristics For example they must be dependable efficient meet real time constraints and require customized user interfaces instead of generic keyboard and mouse interfaces Therefore it makes sense to consider common principles of embedded system design Embedded System Design starts with an introduction into the area and a survey of specification models and languages for embedded and cyber physical systems It provides a brief overview of hardware devices used for such systems and presents the essentials of system software for embedded systems like real time operating systems The book also discusses evaluation and validation techniques for embedded systems Furthermore the book presents an overview of techniques for mapping applications to execution platforms Due to the importance of resource

efficiency the book also contains a selected set of optimization techniques for embedded systems including special compilation techniques The book closes with a brief survey on testing Embedded System Design can be used as a text book for courses on embedded systems and as a source which provides pointers to relevant material in the area for PhD students and teachers It assumes a basic knowledge of information processing hardware and software Courseware related to this book is available at http ls12 www cs tu dortmund de marwedel *Programming Multicore and Many-core Computing Systems* Sabri Pllana,Fatos Xhafa,2017-01-23 Programming multi core and many core computing systems Sabri Pllana Linnaeus University Sweden Fatos Xhafa Technical University of Catalonia Spain Provides state of the art methods for programming multi core and many core systems The book comprises a selection of twenty two chapters covering fundamental techniques and algorithms programming approaches methodologies and frameworks scheduling and management testing and evaluation methodologies and case studies for programming multi core and many core systems Program development for multi core processors especially for heterogeneous multi core processors is significantly more complex than for single core processors However programmers have been traditionally trained for the development of sequential programs and only a small percentage of them have experience with parallel programming In the past only a relatively small group of programmers interested in High Performance Computing HPC was concerned with the parallel programming issues but the situation has changed dramatically with the appearance of multi core processors on commonly used computing systems It is expected that with the pervasiveness of multi core processors parallel programming will become mainstream The pervasiveness of multi core processors affects a large spectrum of systems from embedded and general purpose to high end computing systems This book assists programmers in mastering the efficient programming of multi core systems which is of paramount importance for the software intensive industry towards a more effective product development cycle Key features Lessons challenges and roadmaps ahead Contains real world examples and case studies Helps programmers in mastering the efficient programming of multi core and many core systems The book serves as a reference for a larger audience of practitioners young researchers and graduate level students A basic level of programming knowledge is required to use this book **Embedded Computing for High Performance** João Manuel Paiva Cardoso,José Gabriel de Figueiredo Coutinho,Pedro C. Diniz,2017-06-13 Embedded Computing for High Performance Design Exploration and Customization Using High level Compilation and Synthesis Tools provides a set of real life example implementations that migrate traditional desktop systems to embedded systems Working with popular hardware including Xilinx and ARM the book offers a comprehensive description of techniques for mapping computations expressed in programming languages such as C or MATLAB to high performance embedded architectures consisting of multiple CPUs GPUs and reconfigurable hardware FPGAs The authors demonstrate a domain specific language LARA that facilitates retargeting to multiple computing systems using the same source code In this way users can decouple original application code from transformed code and enhance productivity and program portability

After reading this book engineers will understand the processes methodologies and best practices needed for the development of applications for high performance embedded computing systems Focuses on maximizing performance while managing energy consumption in embedded systems Explains how to retarget code for heterogeneous systems with GPUs and FPGAs Demonstrates a domain specific language that facilitates migrating and retargeting existing applications to modern systems Includes downloadable slides tools and tutorials   Handbook of Research on Computational Science and Engineering: Theory and Practice Leng, J.,Sharrock, Wes,2011-10-31 By using computer simulations in research and development computational science and engineering CSE allows empirical inquiry where traditional experimentation and methods of inquiry are difficult inefficient or prohibitively expensive The Handbook of Research on Computational Science and Engineering Theory and Practice is a reference for interested researchers and decision makers who want a timely introduction to the possibilities in CSE to advance their ongoing research and applications or to discover new resources and cutting edge developments Rather than reporting results obtained using CSE models this comprehensive survey captures the architecture of the cross disciplinary field explores the long term implications of technology choices alerts readers to the hurdles facing CSE and identifies trends in future development   **Proceedings** ,2005   **CODES+ISSS ...** ,2006   *The Software Optimization Cookbook* Richard Gerber,Aart J. C. Bik,Kevin Smith,Xinmin Tian,2006 Annotation Four Intel experts explain the techniques and tools that you can use to improve the performance of applications for IA 32 processors Simple explanations and code examples help you to develop software that benefits from Intel Extended Memory 64 Technology Intel EM64T multi core processing Hyper Threading Technology OpenMP and multimedia extensions This book guides you through the growing collection of software tools compiler switches and coding optimizations showing you efficient ways to get the best performance from software applications   *The Software Encyclopedia 2000* Bowker Editorial Staff,2000-05
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