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Xiaolong Qi



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Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs Steven T. Karris, 2007 This book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits It is self contained CRC Handbook of Digital System Design, Second Edition Wen C. Lin, 1990-09-25 As technology advances digital system designers must acquire and maintain skills to design systems with analog pulse time and digital circuits along with LSI and VLSI devices The CRC Handbook of Digital System Design Second Edition reviews the fundamentals of these topics for the convenience of designers who need to refresh their memories from time to time In a somewhat unique presentation this book integrates theory with practical design and covers three broad topics The basics formulas design equation terminology symbols and notation Characteristics properties and principles of operation of devices modules and building blocks frequently used as components in digital system design Design procedures guidelines for system design presented through examples The author includes numerous examples both simple and complex throughout the book that help clarify points often confusing or overlooked He also addresses memory and arithmetic unit design techniques of grounding and shielding for analog and digital noise and graphical techniques for nonlinear circuits and transmission line analysis The style is straightforward the treatment self contained and practical The CRC Handbook of Digital System Design Second Edition remains a popular and valuable resource for anyone involved in digital system design 2-[mu] M CMOS Standard Cell Data Book Texas Instruments Incorporated, 1986 *Advanced CMOS Logic Data Book*, 1990 **IC Master**, 2001 **The Digital IC Handbook** M. S. Morley, 1988 *ALS/AS Logic Data Book*, 1995 *ALS/AS Logic Data Book* Texas Instruments Incorporated, 1986 *The TTL Data Book for Design Engineers* Texas Instruments Incorporated, 1976 **Supplement to the TTL Data Book for Design Engineers** Texas Instruments Incorporated, 1974 **Digital Electronics** Franco Zappa, 2019-09-11 The book is addressed to an audience interested in the hardware design of digital electronic circuits and systems It introduces the basics of digital electronics and then describes in detail both combinational and sequential logics and components The book aims at providing an in depth overview of the devices and components necessary to design digital electronic systems by exploiting commercially available components The book describes the most important concepts components internal block diagrams schematics and functional specifications implementations and design tricks that are the fundamental building blocks of any complex electronic system designed to be implemented either through discrete components in electronic boards or by means of single chip programmable logic such as Field Programmable Gate Arrays and microcontrollers The topics covered by the book are Basic and advanced logic gates TTL and CMOS logic families and interoperability Combinational logic and truth table Sum of Products Product of Sums and Karnaugh maps design Sequential logic and classifications Latches and Flip Flops Combinational MSI integrated circuits encoders decoders comparators parity generators and checkers adders ALU multiplexer demultiplexer Sequential MSI integrated

circuits latches and flip flops registers shift registers counters Memories ROM RAM SDRAM E2PROM and flash Basics on 8 bit Microcontrollers

Digital Electronic Components Franco Zappa, Rita Asquini, 2025-05-13 The book is aimed at readers interested in the hardware design of digital electronic circuits and systems It introduces the basics of digital electronics and then describes in detail both combinational and sequential components The book aims at providing an indepth overview of commercially available off the shelf devices necessary to design low and medium complexity digital electronic systems Topics covered in the book Basic and advanced logic gates TTL and CMOS logic families and interoperability Combinational logic and truth table Sum of Products Product of Sums and Karnaugh maps design Sequential logic and classifications Latches and Flip Flops Combinational components encoders decoders comparators parity generators and checkers Adders ALU mux demux Sequential components latches and flip flops shift registers counters Memories ROM RAM SDRAM and flash 8 bit Microcontrollers architectures and development tools Basics of typical microcontroller s assembly language and opcodes Microcontrollers internal peripherals and their working operations

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