Processing System

Flash Controller NOR, NAND, SRAM, Quad SPI

Multiport DRAM Controller DDR3, DDR3L, DDR2

Processor I/O Mux

AMBA® Interconnect

AMBA® Interconnect

2x SPI
2x I2C
2x CAN
2x UART
GPIO
2x SDIO with DMA
2x USB with DMA
2x GigE with DMA

MPCore

NEON™ SIMO and FPU	NEON™ SIMD and FPU
ARM® Cortex™ - A9	ARM® Cortex™ - A9

Snoop Control Unit

512KB L2 Cache	256KB On-Chip Memory

GIC	JTAG and Trace	Configuration	Timers	DMA

AMBA® Interconnect

Security AES, SHA, RSA

AMBA® Interconnect

EMIO

General-Purpose AXI Ports

ACP

High-Performance AXI Ports

Programmable Logic
(System Gates, DSP, RAM)

XADC 2x ADC, Mux, Thermal Sensor

PCIe® Gen 2 1–8 Lanes

Multi-Standard I/Os (3.3V & High Speed 1.8V)

Serial Transceivers

Stack Overview Xilinx

Jianjun Gao

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