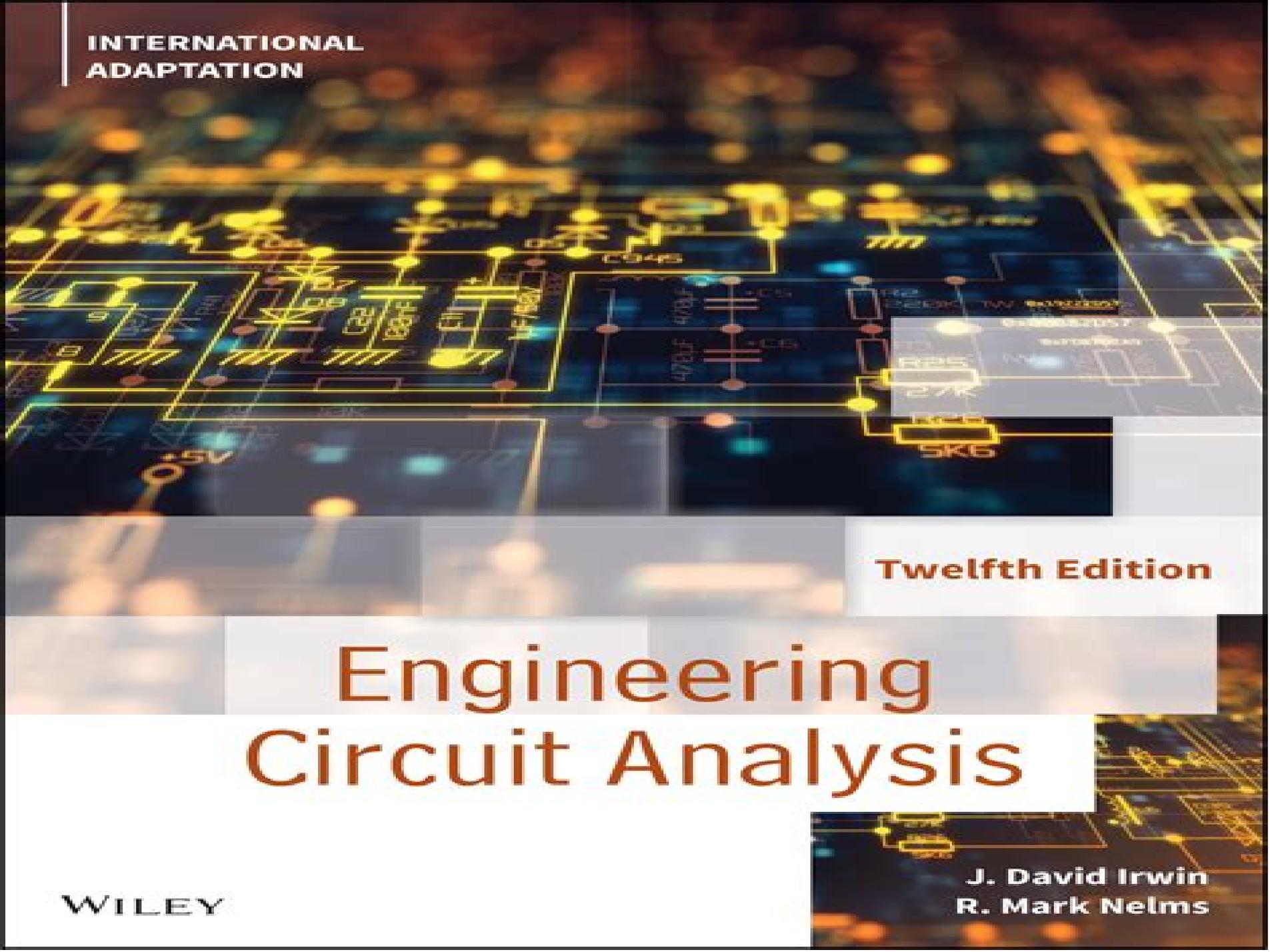


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# Engineering Circuit Analysis

WILEY

J. David Irwin  
R. Mark Nelms

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