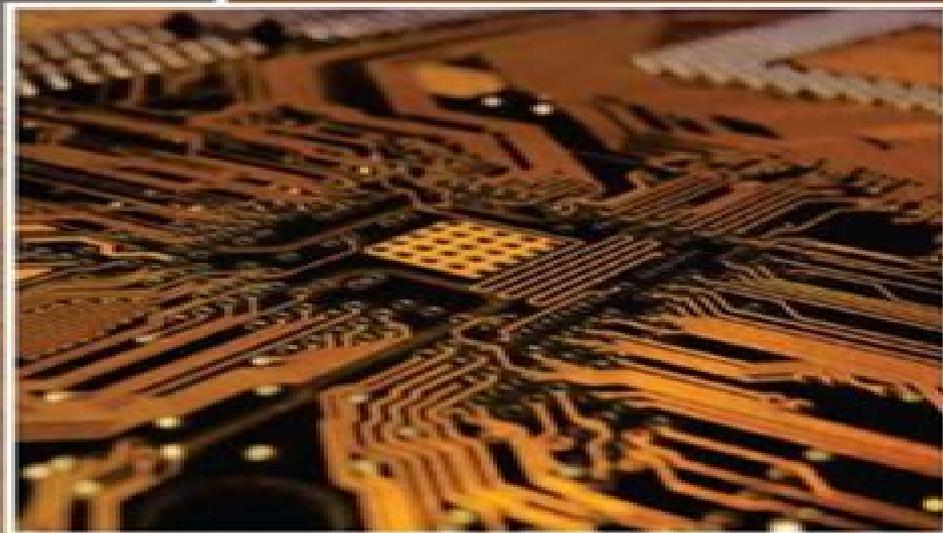


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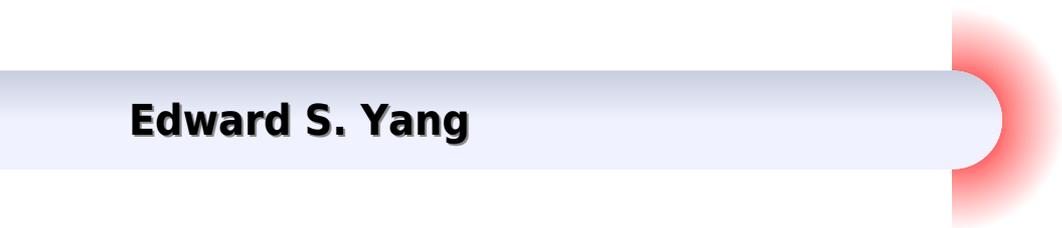
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Fundamentals of Semiconductor Devices Edward S. Yang,1978 **Analysis and Design of MOSFETs** Jun Jie Liou,Adelmo Ortiz-Conde,Francisco Garcia-Sanchez,2012-12-06 Analysis and Design of MOSFETs Modeling Simulation and Parameter Extraction is the first book devoted entirely to a broad spectrum of analysis and design issues related to the semiconductor device called metal oxide semiconductor field effect transistor MOSFET These issues include MOSFET device physics modeling numerical simulation and parameter extraction The discussion of the application of device simulation to the extraction of MOSFET parameters such as the threshold voltage effective channel lengths and series resistances is of particular interest to all readers and provides a valuable learning and reference tool for students researchers and engineers Analysis and Design of MOSFETs Modeling Simulation and Parameter Extraction extensively referenced and containing more than 180 illustrations is an innovative and integral new book on MOSFETs design technology **Lithium-ion Battery**

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Single-chip Bluetooth Solutions Sudepto Chakraborty,2001

Multi-Threshold CMOS

Digital Circuits Mohab Anis,Mohamed Elmasry,2012-12-06 Multi Threshold CMOS Digital Circuits Managing Leakage Power discusses the Multi threshold voltage CMOS MTCMOS technology that has emerged as an increasingly popular technique to control the escalating leakage power while maintaining high performance The book addresses the leakage problem in a number of designs for combinational sequential dynamic and current steering logic Moreover computer aided design methodologies for designing low leakage integrated circuits are presented The book give an excellent survey of state of the art techniques presented in the literature as well as proposed designs that minimize leakage power while achieving high performance Multi Threshold CMOS Digital Circuits Managing Leakage Power is written for students of VLSI design as well as practicing circuit designers system designers CAD tool developers and researchers It assumes a basic knowledge of digital circuit design and device operation and covers a broad range of circuit design techniques

Semiconductor

fundamentals Robert F. Pierret,1983

Charge Transport in Low Dimensional Semiconductor Structures Vito Dario

Camiola,Giovanni Mascali,Vittorio Romano,2020-03-02 This book offers from both a theoretical and a computational perspective an analysis of macroscopic mathematical models for description of charge transport in electronic devices in particular in the presence of confining effects such as in the double gate MOSFET The models are derived from the semiclassical Boltzmann equation by means of the moment method and are closed by resorting to the maximum entropy principle In the case of confinement electrons are treated as waves in the confining direction by solving a one dimensional Schrödinger equation obtaining subbands while the longitudinal transport of subband electrons is described semiclassically Limiting energy transport and drift diffusion models are also obtained by using suitable scaling procedures An entire chapter in the book is dedicated to a promising new material like graphene The models appear to be sound and sufficiently accurate for systematic use in computer aided design simulators for complex electron devices The book is addressed to applied mathematicians physicists and electronic engineers It is written for graduate or PhD readers but the opening chapter contains a modicum of semiconductor physics making it self consistent and useful also for undergraduate students

Journal of Heat Transfer ,2005

Semiconductor Fundamentals Robert F. Pierret,1988-01-01

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the 2003 ASME Summer Heat Transfer Conference ,2003 **Solutions Manual for Semiconductor-device Electronics R. M. Warner,B. L. Grung,1991 **Advanced Semiconductor Fundamentals** Robert F. Pierret,1987-01-01**
Properties of Nanostructured Silicon-on-insulator Electric Force Microscopy Emma Rosamond Tevaarwerk,2005
Proceedings, International Test Conference 1997 ,1997 **Semiconductor Device Fundamentals** ,2005
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