

Computer Arithmetic Algorithms



Israel Koren

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Ying-Ying Zheng



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Wireless Security: Models, Threats, and Solutions Randall K. Nichols, Panos C. Lekkas, 2002 Nichols and Lekkas uncover the threats and vulnerabilities unique to the wireless communication, telecom, broadband, and satellite markets. They provide an overview of current commercial security solutions available on the open market.

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countries Name index only c Book News Inc *Computer Arithmetic* Behrooz Parhami,2010 Ideal for graduate and senior undergraduate courses in computer arithmetic and advanced digital design *Computer Arithmetic Algorithms and Hardware Designs* Second Edition provides a balanced comprehensive treatment of computer arithmetic It covers topics in arithmetic unit design and circuit implementation that complement the architectural and algorithmic speedup techniques used in high performance computer architecture and parallel processing Using a unified and consistent framework the text begins with number representation and proceeds through basic arithmetic operations floating point arithmetic and function evaluation methods Later chapters cover broad design and implementation topics including techniques for high throughput low power fault tolerant and reconfigurable arithmetic An appendix provides a historical view of the field and speculates on its future An indispensable resource for instruction professional development and research *Computer Arithmetic Algorithms and Hardware Designs* Second Edition combines broad coverage of the underlying theories of computer arithmetic with numerous examples of practical designs worked out examples and a large collection of meaningful problems This second edition includes a new chapter on reconfigurable arithmetic in order to address the fact that arithmetic functions are increasingly being implemented on field programmable gate arrays FPGAs and FPGA like configurable devices Updated and thoroughly revised the book offers new and expanded coverage of saturating adders and multipliers truncated multipliers fused multiply add units overlapped quotient digit selection bipartite and multipartite tables reversible logic dot notation modular arithmetic Montgomery modular reduction division by constants IEEE floating point standard formats and interval arithmetic **Eleventh International Conference on VLSI Design** VLSI Society of India,IEEE Circuits and Systems Society,1997 Areas covered in this work include physical design synthesis delay test and timing high level synthesis hardware software co design low power design verification VLSI synthesis testability enhancement asynchronous design diagnosis test and fault modelling and mixed signal design Mathematical Reviews ,1994 Proceedings Earl E. Swartzlander (Jr.),Mary Jane Irwin,Graham Jullien,1993 Proceedings of the symposium held at Windsor Ontario Canada June July 1993 The 34 refereed papers address topics in number systems residue arithmetic multipliers and dot products division and square root elementary function evaluation arithmetic processor design algorithms circuit technology compilers and languages and cryptography Th **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology** ,1999 *Conference Record* ,2001 **CASES ...** ,2005 Energy Research Abstracts ,1983

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