

# HND Level 100 - Computer Architecture

## Structured Questions and Answers

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### Section A: Internal Structure and Operation of a Microprocessor (20 Marks)

1. a. Define the term microprocessor and list two of its major functions. (4 marks)

**Answer:**

A microprocessor is an integrated circuit that performs arithmetic, logic, control, and input/output operations of a computer system. It acts as the brain of a computer.

**Functions:**

- Performs arithmetic and logical operations
- Controls other components of the computer system

b. Describe the internal structure of a basic microprocessor with a labeled diagram. (6 marks)

**Answer:**

The internal structure typically includes:

- Arithmetic Logic Unit (ALU)
- Control Unit
- Registers
- Buses (data, address, control)

[Include labeled diagram manually in class or on whiteboard]

c. Explain the function of the following microprocessor components: (6 marks)

i. ALU

Performs arithmetic and logic operations.

ii. Control Unit

Directs the operations of the processor.

iii. Registers

Temporary storage locations for instructions and data.

d. Outline the fetch-decode-execute cycle. (4 marks)

**Answer:**

- Fetch: Retrieve instruction from memory.
- Decode: Interpret the instruction.
- Execute: Carry out the instruction.

### Section B: Computer Memory (20 Marks)

2. a. Define computer memory and distinguish between primary and secondary memory. (4 marks)

# Computer Architecture Questions And Answers Objectives

**N Noddings**



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**Fifth Generation Computer Systems** T. Moto-Oka,2012-12-02 The Japan Information Processing Development Centre JIPDEC established a committee for Study and Research on Fifth Generation Computers Beginning in 1979 this Committee set out on a two year investigation into the most desirable types of computer systems for application in the 1990 s fifth generation computers and how the development

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Japanese Technological Advances and Possible United States Responses Using Research Joint Ventures United States. Congress. House. Committee on Science and Technology. Subcommittee on Investigations and Oversight,1984

*MCSE Designing a Microsoft Windows 2000 Directory Services Infrastructure Readiness Review; Exam 70-219* Jill Spealman,2001

**Department of Defense Appropriations for 2000** United States. Congress. House. Committee on Appropriations. Subcommittee on Department of Defense,2004

**Proceedings of the Third IEEE Conference on Fuzzy Systems** ,1994

Fifth Generation Computer Systems Tohru Moto-oka,1982 Keynote speech Overview report Knowledge information processing research plan Architecture research plan Invited lectures knowledge information processing Invited lectures architecture Major contributions of research and planning committee of fifth generation computer systems 1979 1981 Autor index

*Documentation Abstracts* ,1991

The No-nonsense Guide to Computing Careers Marc Rettig,1992

**Communication Networks in Health Care** International Medical Informatics Association,1982

**EDN, Electrical Design News** ,1978

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