

- 2.1 Consider the memoryless system with characteristics shown in Fig 2.19, in which u denotes the input and y the output. Which of them is a linear system? Is it possible to introduce a new output so that the system in Fig 2.19(b) is linear?

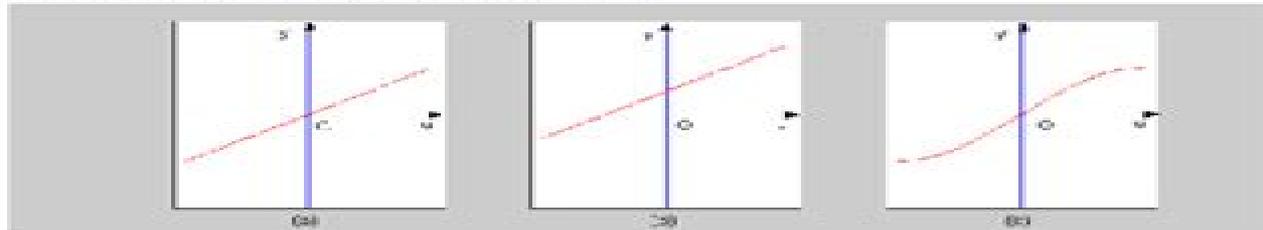


Figure 2.19

Translation: 考虑具有图 2.19 中表示的特性的无记忆系统。其中 u 表示输入， y 表示输出。下面哪一个为线性系统？可以找到一个新的输出，使得图 2.19(b) 中的系统是线性的吗？

Answer: The input-output relation in Fig 2.1(a) can be described as:

$$y = a * u$$

Here a is a constant. It is a memoryless system. Easy to testify that it is a linear system. The input-output relation in Fig 2.1(b) can be described as:

$$y = a * u + b$$

Here a and b are all constants. Testify whether it has the property of additivity. Let:

$$y_1 = a * u_1 + b$$

$$y_2 = a * u_2 + b$$

then:

$$(y_1 + y_2) = a * (u_1 + u_2) + 2 * b$$

So it does not has the property of additivity, therefore, is not a linear system. But we can introduce a new output so that it is linear. Let:

$$z = y - b$$

$$z = a * u$$

z is the new output introduced. Easy to testify that it is a linear system.

The input-output relation in Fig 2.1(c) can be described as:

$$y = \alpha(u) * u$$

$\alpha(u)$ is a function of input u . Choose two different input, get the outputs:

$$y_1 = \alpha_1 * u_1$$

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