



# CMOS

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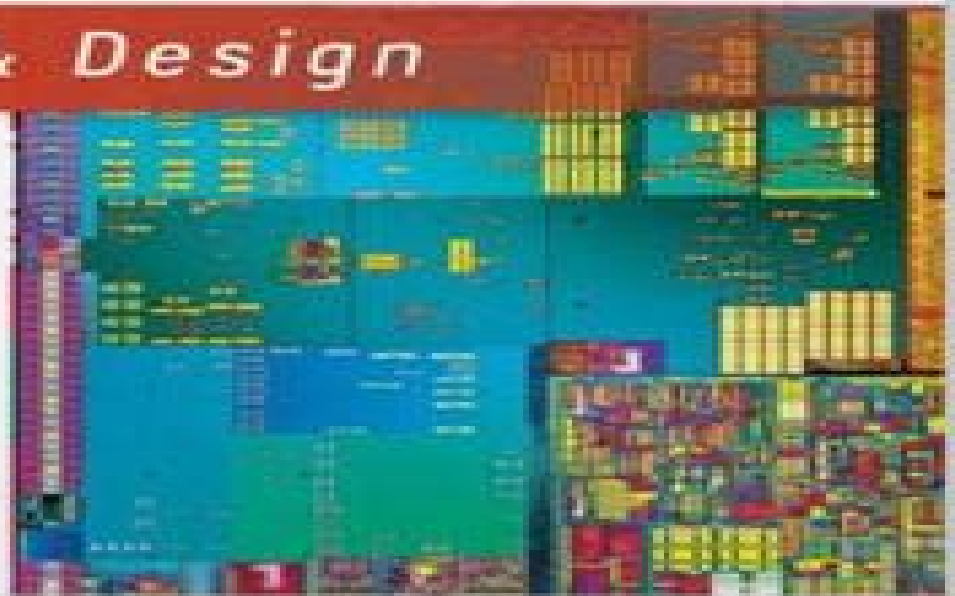
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# Cmos Digital Integrated Circuits Solutions

**Jiyuan Zhang**



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shaping the trajectory of future research endeavors in VLSI This book covers topics such as electrical engineering optimization techniques and computer science and is a useful resource for engineers computer scientists academicians and researchers CMOS Digital IC James B. Kuo,1996 MOSFET Technologies for Double-Pole Four-Throw Radio-Frequency Switch Viranjay M. Srivastava,Ghanshyam Singh,2013-10-07 This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double Pole Four Throw DP4T RF switches for next generation communication systems The authors discuss the design of the DP4T RF switch by using the Double Gate DG MOSFET as well as the Cylindrical Surrounding double gate CSDG MOSFET The effect of HFO2 high dielectric material in the design of DG MOSFET and CSDG MOSFET is also explored Coverage includes comparison of Single gate MOSFET and Double gate MOSFET switching parameters as well as testing of MOSFETs parameters using image acquisition *Digital Integrated Circuits* Thomas A. DeMassa,Zack Ciccone,1996 Contains the most extensive coverage of digital integrated circuits available in a single source Provides complete qualitative descriptions of circuit operation followed by in depth analytical analyses and spice simulations The circuit families described in detail are transistor transistor logic TTL STTL and ASTTL emitter coupled logic ECL NMOS logic CMOS logic dynamic CMOS BiCMOS structures and various GASFET technologies In addition to detailed presentation of the basic inverter circuits for each digital logic family complete details of other logic circuits for these families are presented Quality Control and Reliability International Association of Science and Technology for Development,1987 **Analysis and Design of Digital Integrated Circuits** David A. Hodges,Horace G. Jackson,1988 This is a state of the art treatment of the circuit design of digital integrated circuits It includes coverage of the basic concepts of static characteristics voltage transfer characteristics noise margins fanout power dissipation and dynamic characteristics propagation delay times and the interrelationships among these parameters The authors are regarded as leading authorities in integrated circuits and MOS technology Subject Guide to Books in Print ,1993 **Scientific and Technical Aerospace Reports** ,1977 **Automatic Synthesis of CMOS Digital/analog Converters** Robert McKinstry Robinson Neff,1995 *Proceedings of the ... International Conference on Microelectronics* ,2003 *Device Physics, Modeling, Technology, and Analysis for Silicon MESFET* Iraj Sadegh Amiri,Hossein Mohammadi,Mahdiar Hosseinghadiry,2018-12-13 This book provides detailed and accurate information on the history structure operation benefits and advanced structures of silicon MESFET along with modeling and analysis of the device The authors explain the detailed physics that are important in modeling of SOI MESFETs and present the derivations of compact model expressions so that users can recognize the physical meaning of the model equations and parameters The discussion also includes advanced structures for SOI MESFET for submicron applications Electronic Design ,1984 *Proceedings* ,2000 *The Electron and the Bit* John V. Gutttag,2005 **13th Symposium on Integrated Circuits and Systems Design** Ricardo Augusto da Luz Reis,Jose Carlos Monteiro,Wilhelmus Van Noije,2000 These papers are taken from 13th Brazilian Symposium on

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