DIGITAL DESIGN USING FIELD PROGRAMMABLE GATE ARRAYS

Pak K. Chan / Samiha Mourad

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Digital Design Using Field Programmable Gate Arrays Pak K. Chan,Samiha Mourad,1994 For graduate and undergraduate students as well as professionals in the field of digital design This is the first book to offer a complete description of FPGAs and the methods involved in using CAD design tools for implementation of digital systems using FPGAs It covers both general concepts of systems and logic design and specific issues related to FPGAs themselves with reference to all existing technologies KEY TOPICS Provides a complete approach to digital systems specification synthesis implementation and prototyping Outlines all steps in using FPGA technology in logic design from description of the problem to realization and contains practical detailed examples throughout   Digital System Design with Field Programmable Gate Arrays Pak K. Chan,1994   *Field-Programmable Gate Arrays* John V. Oldfield,Richard C. Dorf,1995-01-23 Due to unique advantages like security improved testing and reprogrammability field programmable gate arrays are making broad inroads in the electronics industry This comprehensive overview of the topic explains the underlying principles strengths and limitations of a range of FPGA architectures Includes abundant references and illustrations   **Field-Programmable Gate Arrays** John Oldfield,Richard Dorf,1995 Timely authoritative application oriented an in depth exploration of current and future uses of FPGAs in digital systems The development of field programmable gate arrays FPGAs may well be the most important breakthrough for the microelectronics industry since the invention of the microprocessor Using FPGAs a system designer working on a PC can now develop a working prototype in a few hours and change it at will in just a few minutes rather than waiting weeks or months for a printed circuit assembly or a custom integrated circuit to be built This newfound ability to change a system by simply altering its configuration memory is also leading to exciting new forms of computing such as array applications that exploit parallelism Now in a book that functions equally well as a working professional reference and a pedagogically consistent computer engineering text John V Oldfield and Richard C Dorf Provide a detailed overview of FPGAs in digital systems design Explain the underlying principles strengths and limitations of most FPGA architectures Supply many real life case studies from elementary to advanced applications including examples of custom computing machines Review cutting edge developments including new architectures and a new field programmable interconnect chip Discuss key economic and business aspects of FPGA manufacture and applications and their role in intellectual property protection Demonstrate ways in which FPGAs offer plausible solutions to some of the major computing problems of our day   *Field-Programmable Gate Arrays* Stephen D. Brown,Robert J. Francis,Jonathan Rose,Zvonko G. Vranesic,2012-12-06 Field Programmable Gate Arrays FPGAs have emerged as an attractive means of implementing logic circuits providing instant manufacturing turnaround and negligible prototype costs They hold the promise of replacing much of the VLSI market now held by mask programmed gate arrays FPGAs offer an affordable solution for customized VLSI over a wide variety of applications and have also opened up new possibilities in designing reconfigurable digital systems Field

Programmable Gate Arrays discusses the most important aspects of FPGAs in a textbook manner It provides the reader with a focused view of the key issues using a consistent notation and style of presentation It provides detailed descriptions of commercially available FPGAs and an in depth treatment of the FPGA architecture and CAD issues that are the subjects of current research The material presented is of interest to a variety of readers including those who are not familiar with FPGA technology but wish to be introduced to it as well as those who already have an understanding of FPGAs but who are interested in learning about the research directions that are of current interest **Applications of Field-Programmable Gate Arrays in Scientific Research** Hartmut F.-W. Sadrozinski,Jinyuan Wu,2016-04-19 Focusing on resource awareness in field programmable gate array FPGA design Applications of Field Programmable Gate Arrays in Scientific Research covers the principle of FPGAs and their functionality It explores a host of applications ranging from small one chip laboratory systems to large scale applications in big science The book first de **Field-Programmable Gate Array Technology** Stephen M. Trimberger,Stephen Trimberger,1994-01-31 Many different kinds of FPGAs exist with different programming technologies different architectures and different software Field Programmable Gate Array Technology describes the major FPGA architectures available today covering the three programming technologies that are in use and the major architectures built on those programming technologies The reader is introduced to concepts relevant to the entire field of FPGAs using popular devices as examples Field Programmable Gate Array Technology includes discussions of FPGA integrated circuit manufacturing circuit design and logic design It describes the way logic and interconnect are implemented in various kinds of FPGAs It covers particular problems with design for FPGAs and future possibilities for new architectures and software This book compares CAD for FPGAs with CAD for traditional gate arrays It describes algorithms for placement routing and optimization of FPGAs Field Programmable Gate Array Technology describes all aspects of FPGA design and development For this reason it covers a significant amount of material Each section is clearly explained to readers who are assumed to have general technical expertise in digital design and design tools Potential developers of FPGAs will benefit primarily from the FPGA architecture and software discussion Electronics systems designers and ASIC users will find a background to different types of FPGAs and applications of their use **FPGA 95: ACM/SIGDA International Symposium on Field Programmable Gate Arrays** Pak K. Chan,1995 **Reconfigurable Field Programmable Gate Arrays for Mission-Critical Applications** Niccolò Battezzati,Luca Sterpone,Massimo Violante,2010-11-09 Embedded systems applications that are either mission or safety critical usually entail low to mid production volumes require the rapid development of specific tasks which are typically computing intensive and are cost bounded The adoption of re configurable FPGAs in such application domains is constrained to the availability of suitable techniques to guarantee the dependability requirements entailed by critical applications This book describes the challenges faced by designers when implementing a mission or safety critical application using re configurable FPGAs and it details various techniques to overcome these

challenges In addition to an overview of the key concepts of re configurable FPGAs it provides a theoretical description of the failure modes that can cause incorrect operation of re configurable FPGA based electronic systems It also outlines analysis techniques that can be used to forecast such failures and covers the theory behind solutions to mitigate fault effects This book also reviews current technologies available for building re configurable FPGAs specifically SRAM based technology and Flash based technology For each technology introduced theoretical concepts presented are applied to real cases Design techniques and tools are presented to develop critical applications using commercial off the shelf devices such as Xilinx Virtex FPGAs and Actel ProASIC FPGAs Alternative techniques based on radiation hardened FPGAs such as Xilinx SIRF and Atmel ATF280 are also presented This publication is an invaluable reference for anyone interested in understanding the technologies of re configurable FPGAs as well as designers developing critical applications based on these technologies

Logic Synthesis for Field-Programmable Gate Arrays Rajeev Murgai,Robert K. Brayton,Alberto Sangiovanni-Vincentelli,1995-07-31 Short turnaround has become critical in the design of electronic systems Software programmable components such as microprocessors and digital signal processors have been used extensively in such systems since they allow rapid design revisions However the inherent performance limitations of software programmable systems mean that they are inadequate for high performance designs Designers thus turned to gate arrays as a solution User programmable gate arrays field programmable gate arrays FPGAs have recently emerged and are changing the way electronic systems are designed and implemented The growing complexity of the logic circuits that can be packed onto an FPGA chip means that it has become important to have automatic synthesis tools that implement logic functions on these architectures Logic Synthesis for Field Programmable Gate Arrays describes logic synthesis for both look up table LUT and multiplexor based architectures with a balanced presentation of existing techniques together with algorithms and the system developed by the authors Audience A useful reference for VLSI designers developers of computer aided design tools and anyone involved in or with FPGAs   **Field Programmable Gate Arrays (FPGAs)** Richard Larry Ukeiley,1993 Aims to condense all the information needed to design with FPGAs into one easy to understand package It covers the design of a FPGA system manual editing tuning of a FPGA device integrating a FPGA device into a system and the future architectural issues of FPGAs and their tools   **Field Programmable Gate Array and Applications** S. S. S. P. Rao,2016 Field programmable gate array FPGAs belong to the family of programmable logic devices and designing with FPGAs require knowledge of digital design The book begins with an overview of boolean algebra and logic design followed by topics on programmable logic devices Introduction to field programmable devices is then explained right from the basic FPGA which was used as glue logic to present day very advanced FPGA used in Embedded Systems including military and space applications Further it goes on to discuss the evolution of Xilinx Altera Intel and Actel Microsemi FPGAs and their architectural features and includes many design examples in VHDL and a brief introduction to System Verilog The last three

chapters discuss the Xilinx FPGA design flow completely from architectural specification to obtaining a bit stream to be loaded into the FPGA Testing methodologies and design exercises using Spartan Series and the important research issues of FPGA Security along with the Future of FPGAs for the next two decades are described as well **Hardware/software Co-design Using Field Programmable Gate Arrays and Reconfigurable Controllers** Darrin M. Hanna,2000

Advanced Digital System Design Using SoC FPGAs Ross K. Snider,2023 This textbook teaches students techniques for the design of advanced digital systems using System on Chip SoC Field Programmable Gate Arrays FPGAs The author demonstrates design of custom hardware components for the FPGA fabric using VHDL Matlab and Simulink with implementation of custom hardware software interfaces Readers gain hands on system level experience by creating custom hardware and then writing programs and Linux device drivers to interact with their hardware This textbook enables laboratory experience in the design of custom digital systems using SoC FPGAs that emphasizes digital signal processing and culminates in the reader creating a real time audio sound effects processor Uses an Integrated Hardware Software Approach to teaching Digital Design Walks readers through the creation of a complete custom computer system that can accelerate audio signal processing Enables students to develop a system level view of computing and develop skills to create custom hardware and software **Applications of Digital Signal Processing Using Field Programmable Gate Arrays (FPGAs)** Chris Gillette,2001 **Logic Synthesis for Field-Programmable Gate Arrays** Rajeev Murgai,Robert K. Brayton,Alberto Sangiovanni-Vincentelli,2012-10-08 Short turnaround has become critical in the design of electronic systems Software programmable components such as microprocessors and digital signal processors have been used extensively in such systems since they allow rapid design revisions However the inherent performance limitations of software programmable systems mean that they are inadequate for high performance designs Designers thus turned to gate arrays as a solution User programmable gate arrays field programmable gate arrays FPGAs have recently emerged and are changing the way electronic systems are designed and implemented The growing complexity of the logic circuits that can be packed onto an FPGA chip means that it has become important to have automatic synthesis tools that implement logic functions on these architectures Logic Synthesis for Field Programmable Gate Arrays describes logic synthesis for both look up table LUT and multiplexor based architectures with a balanced presentation of existing techniques together with algorithms and the system developed by the authors Audience A useful reference for VLSI designers developers of computer aided design tools and anyone involved in or with FPGAs **Design and Implementation of a 15,000 Gate Field Programmable Gate Array in a Distributed Digital Control System** Brian C. Boorman,1998 Digital Design of a Field Programmable Gate Array for Data Acquisition and Communications in an Embedded System Mike Gibson,2003 *Field Programmable Gate Arrays (FPGAs) II* George Dekoulis,2020-09-09 This Edited Volume Field Programmable Gate Arrays FPGAs II is a collection of reviewed and relevant research chapters offering a comprehensive overview of recent developments in the field of

Computer and Information Science The book comprises single chapters authored by various researchers and edited by an expert active in the Computer and Information Science research area All chapters are complete in itself but united under a common research study topic This publication aims at providing a thorough overview of the latest research efforts by international authors on Computer and Information Science and open new possible research paths for further novel developments   Field Programmable Gate Array for Implementation of Redundant Advanced Digital Feedback Control National Aeronautics and Space Administration (NASA),2018-06-20 The goal of this effort was to develop a digital motor controller using field programmable gate arrays FPGAs This is a more rugged approach than a conventional microprocessor digital controller FPGAs typically have higher radiation rad tolerance than both the microprocessor and memory required for a conventional digital controller Furthermore FPGAs can typically operate at higher speeds While speed is usually not an issue for motor controllers it can be for other system controllers Other than motor power only a 3 3 V digital power supply was used in the controller no analog bias supplies were used Since most of the circuit was implemented in the FPGA no additional parts were needed other than the power transistors to drive the motor The benefits that FPGAs provide over conventional designs lower power and fewer parts allow for smaller packaging and reduced weight and cost King K D Marshall Space Flight Center NASA TM 2003 212501 NAS 1 15 212501 M 1076

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