

**Answer the following questions**

*MCQ questions Every question has weight equals 1/2 mark.*

- Which is data manipulation types are:
  - Arithmetic instruction
  - Shift instruction
  - Logical and bit manipulation instructions
  - All of these**
- Arithmetic instruction are used to perform operation on:
  - Numerical data**
  - Non-numerical data
  - Both
  - None
- Floating point representation is used to store
  - Boolean values
  - whole numbers
  - real integers**
  - integers
- The operation executed on data stored in registers is called
  - Macro-operation
  - Micro-operation**
  - Bit-operation
  - Byte-operation
- Gray code equivalent of  $(1000)_2$  is
  - $(1111)_G$**
  - $(1100)_G$
  - $(1000)_G$
  - None of these
- \_\_\_\_\_ is concerned with the structure and behavior of the Computers as seen by the user
  - Computer Implementation
  - Computer Architecture**
  - Computer Organization
  - IOP
- When two numbers of  $n$  digits each are added and the sum occupies  $n+1$  digits this is a condition of
  - Over Flow**
  - Below Flow
  - No Flow
  - None of the above
- One way of constructing a common Bus System is with
  - Multiplexers**
  - Demultiplexers
  - Encoder
  - None of the above

# Computer Architecture Midterm Exam Solution

**RJ Shavelson**



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*Computers, Communications, and Information* Sarah E. Hutchinson, Sarah Hutchinson Clifford, Stacey C. Sawyer, 1998  
Designed for the introduction to computers or computer literacy course found in the business or CIS department at two year and four year schools

**iCEER2014-McMaster Digest** Mohamed Bakr, Ahmed Elsharabasy, 2014-11-18 International Conference on Engineering Education and Research **Science, Technology and Society**, 1982 *Management Information Systems and Business Statistics* Richard Schwindt, 1995 Proceedings of the European Computing Conference Nikos Mastorakis, Valeri Mladenov, Vassiliki T. Kontargyri, 2010-03-16 The European Computing Conference offers a unique forum for establishing new collaborations within present or upcoming research projects exchanging useful ideas presenting recent research results participating in discussions and establishing new academic collaborations linking university with the industry Engineers and Scientists working on various areas of Systems Theory Applied Mathematics Simulation Numerical and Computational Methods and Parallel Computing present the latest findings advances and current trends on a wide range of topics This proceedings volume will be of interest to students researchers and practicing engineers

**World Congress on Medical Physics and Biomedical Engineering 2018** Lenka Lhotska, Lucie Sukupova, Igor Lacković, Geoffrey S. Ibbott, 2018-05-29 This book vol 1 presents the proceedings of the IUPESM World Congress on Biomedical Engineering and Medical Physics a triennially organized joint meeting of medical physicists biomedical engineers and adjoining health care professionals Besides the purely scientific and technological topics the 2018 Congress will also focus on other aspects of professional involvement in health care such as education and training accreditation and certification health technology assessment and patient safety The IUPESM meeting is an important forum for medical physicists and biomedical engineers in medicine and healthcare learn and share knowledge and discuss the latest research outcomes and technological advancements as well as new ideas in both medical physics and biomedical engineering field

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**Architectural Program Report, June 1997** University of California, Berkeley. Department of Architecture, 1997 Televised Higher Education Western Interstate Commission for Higher Education, 1984 THE Catalog is a comprehensive listing of videocourses appropriate for postsecondary level study on a wide range of academic fields

15. **ULUSLARARASI BİLGİSAYAR VE ÖĞRETİM TEKNOLOJİLERİ SEMPOZYUMU-OZETLER KİTABI**, 2022-09-01 15 ULUSLARARASI BİLGİSAYAR VE ÖĞRETİM TEKNOLOJİLERİ SEMPOZYUMU Sanal Evren ve Gerçeklik Sarmalında Etkin ZETLER KİTABI 07-09 EYLÜL 2022

**Catalogs of Courses** University of California, Berkeley, 1994 Includes general and summer catalogs issued between 1878 1879 and 1995 1997 *VLSI Design*, 1982 Learning in the Age of Digital and Green Transition Michael E. Auer, Wolfgang Pachatz, Tiia Rützmann, 2023-03-21 We are currently witnessing a significant transformation in the development of education on all levels and especially in post

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Japanese Current Research ,1987      **Computer Architecture MCQs** Arshad Iqbal,2019-06-14 Computer Architecture Multiple Choice Questions and Answers MCQs Computer architecture quiz questions and answers with practice tests for online exam prep and job interview prep Computer architecture study guide with questions and answers about assessing computer performance computer architecture and organization computer arithmetic computer language and instructions computer memory review computer technology data level parallelism and GPU architecture embedded systems exploiting memory instruction level parallelism instruction set principles interconnection networks memory hierarchy design networks storage and peripherals pipe lining in computer architecture pipe lining performance processor datapath and control quantitative design and analysis request level and data level parallelism storage systems thread level parallelism Computer architecture trivia questions and answers to get prepare for career placement tests and job interview prep with answers key Practice exam questions and answers about computer science composed from computer architecture textbooks on chapters Assessing Computer Performance Practice Test 13 MCQs Computer Architecture and Organization Practice Test 19 MCQs Computer Arithmetic Practice Test 33 MCQs Computer Language and Instructions Practice Test 52 MCQs Computer Memory Review Practice Test 66 MCQs Computer Technology Practice Test 14 MCQs Data Level Parallelism and GPU Architecture Practice Test 38 MCQs Embedded Systems Practice Test 21 MCQs Exploiting Memory Practice Test 29 MCQs Instruction Level Parallelism Practice Test 52 MCQs Instruction Set Principles Practice Test 30 MCQs Interconnection Networks Practice Test 56 MCQs Memory Hierarchy Design Practice Test 37 MCQs Networks Storage and Peripherals Practice Test 20 MCQs Pipelining in Computer Architecture Practice Test 56 MCQs Pipelining Performance Practice Test 15 MCQs Processor Datapath and Control Practice Test 21 MCQs Quantitative Design and Analysis Practice Test 49 MCQs Request Level and Data Level Parallelism Practice Test 32 MCQs Storage Systems Practice Test 43 MCQs Thread Level Parallelism Practice Test 37 MCQs Computer architecture interview questions and answers on 32 bits MIPS addressing addition and subtraction advanced branch prediction advanced techniques and speculation architectural design vectors architecture and networks arrays and pointers basic cache optimization methods basic compiler techniques cache optimization techniques cache

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**Announcement**, 2003 **Computer Architecture MCQs: Multiple Choice Questions and Answers (Quiz & Tests with Answer Keys)** Arshad Iqbal, 2016 [Solutions Manual to Computer Architecture](#) Robert J. Baron, Lee Higbie, 1992-01-01 *Hands on Computer Architecture 1500+ MCQ E-Book* Harry Chaudhary., 2018-09-01 Our 1500 Computer Architecture Questions and Answers focuses on all areas of Computer Architecture subject covering 100 topics in Computer Architecture These topics are chosen from a collection of most authoritative and best reference books on Computer Architecture One should spend 1 hour daily for 15 days to learn and assimilate Computer Architecture

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