

Silicon Processing

for the VLSI Era
Volume 1 – Process Technology

S. Wolf and R.N. Tauber

Silicon Processing For The Vlsi Era Process Technology

**Constantinos G. Vayenas, Ralph E.
White, Maria E. Gamboa-Aldeco**

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solid state technologies form the background of the society in which we live The aim of this book is threefold to present the background physical chemistry on which the technology of semiconductor devices is based secondly to describe specific issues such as the role of defects on the properties of solids and the crucial influence of surface properties and ultimately to look at the physics and chemistry of semiconductor growth processes both at the bulk and thin film level together with some issues relating to the properties of nano devices Divided into five chapters it covers Thermodynamics of solids including phases and their properties and structural order Point defects in semiconductors Extended defects in semiconductors and their interactions with point defects and impurities Growth of semiconductor materials Physical chemistry of semiconductor materials processing With applications across all solid state technologies the book is useful for advanced students and researchers in materials science physics chemistry electrical and electronic engineering It is also useful for those in the semiconductor industry

Silicon Processing for the VLSI Era Stanley Wolf, Richard N. Tauber, 19?? *How Transistor Area Shrank by 1 Million Fold* Howard Tigelaar, 2020-07-15 This book explains in layman s terms how CMOS transistors work The author explains step by step how CMOS transistors are built along with an explanation of the purpose of each process step He describes for readers the key inventions and developments in science and engineering that overcame huge obstacles enabling engineers to shrink transistor area by over 1 million fold and build billions of transistor switches that switch over a billion times a second all on a piece of silicon smaller than a thumbnail

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