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Software Architecture Henry Muccini,Paris Avgeriou,Barbora Buhnova,Javier Camara,Mauro Caporuscio,Mirco Franzago,Anne Koziolek,Patrizia Scandurra,Catia Trubiani,Danny Weyns,Uwe Zdun,2020-09-10 This book constitutes the refereed proceedings of the tracks and workshops which complemented the 14th European Conference on Software Architecture ECSA 2020 held in L Aquila Italy in September 2020 The 30 full papers and 9 short papers presented in this volume were carefully reviewed and selected from 72 submissions Papers presented were accepted into the following tracks and workshops ECSA 2020 Doctoral Symposium track ECSA 2020 Tool Demos track ECSA 2020 Gender Diversity in Software Architecture CASA 3rd International Workshop on Context aware Autonomous and Smart Architecture CSE QUDOS Joint Workshop on Continuous Software Engineering and Quality Aware DevOps DETECT 3rd International Workshop on Modeling Verication and Testing of Dependable Critical Systems FAACS MDE4SA Joint Workshop on Formal Approaches for Advanced Computing Systems and Model Driven Engineering for Software Architecture IoT ASAP 4th International Workshop on Engineering IoT Systems Architectures Services Applications and Platforms SASI4 2nd Workshop on Systems Architectures and Solutions for Industry 4 0 WASA 6th International Workshop on Automotive System Software Architecture The conference was held virtually due to the COVID 19 pandemic   Architecting and Building High-Speed SoCs Mounir Maaref,2022-12-09 Design a high speed SoC while gaining a holistic view of the FPGA design flow and overcoming its challenges Purchase of the print or kindle book includes a free eBook in the PDF format Key FeaturesUse development tools to implement and verify an SoC including ARM CPUs and the FPGA logicOvercome the challenge of time to market by using FPGA SoCs and avoid the prohibitive ASIC NRE costUnderstand the integration of custom logic accelerators and the SoC software and build themBook Description Modern and complex SoCs can adapt to many demanding system requirements by combining the processing power of ARM processors and the feature rich Xilinx FPGAs You ll need to understand many protocols use a variety of internal and external interfaces pinpoint the bottlenecks and define the architecture of an SoC in an FPGA to produce a superior solution in a timely and cost efficient manner This book adopts a practical approach to helping you master both the hardware and software design flows understand key interconnects and interfaces analyze the system performance and enhance it using the acceleration techniques and finally build an RTOS based software application for an advanced SoC design You ll start with an introduction to the FPGA SoCs technology fundamentals and their associated development design tools Gradually the book will guide you through building the SoC hardware and software starting from the architecture definition to testing on a demo board or a virtual platform The level of complexity evolves as the book progresses and covers advanced applications such as communications security and coherent hardware acceleration By the end of this book you ll have learned the concepts underlying FPGA SoCs advanced features and you ll have constructed a high speed SoC targeting a high end FPGA from the ground up What you will learnUnderstand SoC FPGAs main features

advanced buses and interface protocolsDevelop and verify an SoC hardware platform targeting an FPGA based SoCExplore and use the main tools for building the SoC hardware and softwareBuild advanced SoCs using hardware acceleration with custom IPsImplement an OS based software application targeting an FPGA based SoCUnderstand the hardware and software integration techniques for SoC FPGAsUse tools to co debug the SoC software and hardwareGain insights into communication and DSP principles in FPGA based SoCsWho this book is for This book is for FPGA and ASIC hardware and firmware developers IoT engineers SoC architects and anyone interested in understanding the process of developing a complex SoC including all aspects of the hardware design and the associated firmware design Prior knowledge of digital electronics and some experience of coding in VHDL or Verilog and C or a similar language suitable for embedded systems will be required for using this book A general understanding of FPGA and CPU architecture will also be helpful but not mandatory

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