

## Basys 3™ FPGA Board Reference Manual

Revised April 8, 2016

This manual applies to the Basys 3 rev. C

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### Overview

The Basys 3 board is a complete, ready-to-use digital circuit development platform based on the latest Artix™-7 Field Programmable Gate Array (FPGA) from Xilinx®. With its high-capacity FPGA (Xilinx part number XC7A35T-1CPG236C), low overall cost, and collection of USB, VGA, and other ports, the Basys 3 can host designs ranging from introductory combinational circuits to complex sequential circuits like embedded processors and controllers. It includes enough switches, LEDs, and other I/O devices to allow a large number of designs to be completed without the need for any additional hardware, and enough uncommitted FPGA I/O pins to allow designs to be expanded using Digilent Pmods or other custom boards and circuits.

The Artix-7 FPGA is optimized for high performance logic, and offers more capacity, higher performance, and more resources than earlier designs. Artix-7 35T features include:



The Basys 3.

- 33,280 logic cells in 5200 slices (each slice contains four 6-input LUTs and 8 flip-flops)
- 1,800 Kbits of fast block RAM
- Five clock management tiles, each with a phase-locked loop (PLL)
- 90 DSP slices
- Internal clock speeds exceeding 450MHz
- On-chip analog-to-digital converter (XADC)

The Basys 3 also offers an improved collection of ports and peripherals, including:

- 16 user switches
- 4-digit 7-segment display
- 12-bit VGA output
- Digilent USB-JTAG port for FPGA programming and communication
- 16 user LEDs
- Three Pmod ports
- USB-UART Bridge
- USB HID Host for mice, keyboards and memory sticks
- 5 user pushbuttons
- Pmod for XADC signals
- Serial Flash

The Basys 3 works with Xilinx's new high-performance Vivado™ Design Suite. Vivado includes many new tools and design flows that facilitate and enhance the latest design methods. It runs faster, allows better use of FPGA resources, and allows designers to focus their time evaluating design alternatives. The System Edition includes an on-chip logic analyzer, high-level synthesis tool, other cutting-edge tools, and the free WebPACK™ version allows Basys 3 designs to be created at no additional cost.

# Basys 3 Diligent Documentation Reference Diligentinc

**Gerardo Abel Laguna Sánchez**



### **Basys 3 Diligent Documentation Reference Diligentinc:**

*Ultrasonic Distance Measurement Using Basys 3 FPGA Board* Ramazan Kahraman,2019-10-22 System on a Chip SoC technology which has evolved in recent years is developed from different devices A processor several memory and peripheral components are located on a single chip to form today s high performance SoCs with hundreds of IP blocks IP cores are validated design blocks used as part of complex digital designs Those designs are utilizing a hardware description language like VHDL or Verilog In this way time and cost of launching the product are reduced Thanks to SoC the features of computers were able to be reduced to the miniature level Microcontrollers have the features of computer systems on a single chip They are used to collect process and manipulate data in complex projects The complexity of microcontrollers has increased to provide better performance and flexibility to meet customer requirements However it must be able to adapt to operational changes The hardware of a microcontroller can not be changed afterward If subsequent changes are nevertheless necessary these are associated with high additional costs Reconfigurable devices such as FPGAs can reconfigure the hardware to design develop and deploy high performance digital systems With the power of a SoC combined with the flexibility of an FPGA the MC8051 IP Core proves to be a great alternative to purely microcontroller based systems

*La máquina de Post actualizada* Gerardo Abel Laguna Sánchez,2021-06-01 En esta monografía en formato de opusculo se aborda una introducción a las ciencias computacionales con un enfoque práctico En resumen se presenta el desarrollo completo del diseño puesta en marcha y programación del prototipo para una pequeña unidad central de procesamiento CPU a partir de una variante mejorada de la máquina de Post La máquina de Post es un desarrollo teórico similar al de Alan Turing el famoso matemático considerado como uno de los padres de la computación moderna aunque desarrollado por Emil Post en forma enteramente original e independiente La máquina de Post es mucho más simple que la de Turing por lo tanto dada su relativa simplicidad permite introducir a los interesados de manera clara en los conceptos básicos relativos al funcionamiento de prácticamente todo CPU digital moderno

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