



# *Comparing and Contrasting FPGA and Microprocessor System Design and Development*

*By: Karen Parnell, Roger Bryner*

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Programmable Logic Devices offer a cost effective alternative to custom microprocessors due to their generic nature with the added benefits of short time-to-market, no NRE costs, off-the-shelf availability, ability to control inventory in peak and trough times, and ability to reduce total cost of ownership over the lifetime of an end product.

Microprocessor obsolescence is a major concern for many companies. Programmable logic can provide a viable solution to this problem. By using soft core microprocessors embedded within a programmable logic device, not only can you own the processor core for use in any future devices and platforms, but the design can be both flexible and scalable to suit different platforms.

This paper compares and contrasts FPGA and microprocessor system design and development flows with the aim of helping the designer and definer of state-of-the-art electronics systems to make a considered and well-informed architecture decision. The first part of the paper introduces Xilinx and PLDs to people new to programmable logic. Next, the paper describes the benefits PLDs can bring to total cost management, technical advantages of PLDs, and microprocessor design flows and methodologies. The paper then discusses future trends, the use of soft core microprocessors, and how these can be used to mitigate against processor obsolescence.

# Comparing And Contrasting Fpga And Microprocessor System

**Alan Clements**



## **Comparing And Contrasting Fpga And Microprocessor System:**

*Rapid System Prototyping with FPGAs* R. C. Cofer, Benjamin F. Harding, 2011-03-31 The push to move products to market as quickly and cheaply as possible is fiercer than ever and accordingly engineers are always looking for new ways to provide their companies with the edge over the competition Field Programmable Gate Arrays FPGAs which are faster denser and more cost effective than traditional programmable logic devices PLDs are quickly becoming one of the most widespread tools that embedded engineers can utilize in order to gain that needed edge FPGAs are especially popular for prototyping designs due to their superior speed and efficiency This book hones in on that rapid prototyping aspect of FPGA use showing designers exactly how they can cut time off production cycles and save their companies money drained by costly mistakes via prototyping designs with FPGAs first Reading it will take a designer with a basic knowledge of implementing FPGAs to the next level of FPGA use because unlike broad beginner books on FPGAs this book presents the required design skills in a focused practical example oriented manner In the trenches expert authors assure the most applicable advice to practicing engineers Dual focus on successfully making critical decisions and avoiding common pitfalls appeals to engineers pressured for speed and perfection Hardware and software are both covered in order to address the growing trend toward cross pollination of engineering expertise *Annual Meeting of the North American Fuzzy Information Processing*

*Society--NAFIPS*. North American Fuzzy Information Processing Society. Annual Meeting, 2005 **Advanced Computer and Communication Engineering Technology** Hamzah Asyrani Sulaiman, Mohd Azlishah Othman, Mohd Fairuz Iskandar Othman, Yahaya Abd Rahim, Naim Che Pee, 2015-12-28 This book covers diverse aspects of advanced computer and communication engineering focusing specifically on industrial and manufacturing theory and applications of electronics communications computing and information technology Experts in research industry and academia present the latest developments in technology describe applications involving cutting edge communication and computer systems and explore likely future trends In addition a wealth of new algorithms that assist in solving computer and communication engineering problems are presented The book is based on presentations given at ICOCOE 2015 the 2nd International Conference on Communication and Computer Engineering It will appeal to a wide range of professionals in the field including telecommunication engineers computer engineers and scientists researchers academics and students ASEAN

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**FPGA ...**, 2007

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## **Table of Contents Comparing And Contrasting Fpga And Microprocessor System**

1. Understanding the eBook Comparing And Contrasting Fpga And Microprocessor System
  - The Rise of Digital Reading Comparing And Contrasting Fpga And Microprocessor System
  - Advantages of eBooks Over Traditional Books
2. Identifying Comparing And Contrasting Fpga And Microprocessor System
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Comparing And Contrasting Fpga And Microprocessor System
  - User-Friendly Interface
4. Exploring eBook Recommendations from Comparing And Contrasting Fpga And Microprocessor System
  - Personalized Recommendations
  - Comparing And Contrasting Fpga And Microprocessor System User Reviews and Ratings
  - Comparing And Contrasting Fpga And Microprocessor System and Bestseller Lists
5. Accessing Comparing And Contrasting Fpga And Microprocessor System Free and Paid eBooks
  - Comparing And Contrasting Fpga And Microprocessor System Public Domain eBooks
  - Comparing And Contrasting Fpga And Microprocessor System eBook Subscription Services
  - Comparing And Contrasting Fpga And Microprocessor System Budget-Friendly Options

6. Navigating Comparing And Contrasting Fpga And Microprocessor System eBook Formats
  - ePub, PDF, MOBI, and More
  - Comparing And Contrasting Fpga And Microprocessor System Compatibility with Devices
  - Comparing And Contrasting Fpga And Microprocessor System Enhanced eBook Features
7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Comparing And Contrasting Fpga And Microprocessor System
  - Highlighting and Note-Taking Comparing And Contrasting Fpga And Microprocessor System
  - Interactive Elements Comparing And Contrasting Fpga And Microprocessor System
8. Staying Engaged with Comparing And Contrasting Fpga And Microprocessor System
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Comparing And Contrasting Fpga And Microprocessor System
9. Balancing eBooks and Physical Books Comparing And Contrasting Fpga And Microprocessor System
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Comparing And Contrasting Fpga And Microprocessor System
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
11. Cultivating a Reading Routine Comparing And Contrasting Fpga And Microprocessor System
  - Setting Reading Goals Comparing And Contrasting Fpga And Microprocessor System
  - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Comparing And Contrasting Fpga And Microprocessor System
  - Fact-Checking eBook Content of Comparing And Contrasting Fpga And Microprocessor System
  - Distinguishing Credible Sources
13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
14. Embracing eBook Trends
  - Integration of Multimedia Elements

- Interactive and Gamified eBooks

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web as is common in mathematics we can refer to a set by naming it with a letter for example  $s$   $f_x$   $0$   $x$   $1g$  definition 2 an object  $x$  is an element or member of a set  $s$  written  $x \in s$  if  $x$  satisfies the rule defining membership in  $s$  we can write  $x \notin s$  if  $x$  is not an element of  $s$  definition 3 the empty set or null set denoted or  $\emptyset$  is the

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