nuvoTon

Preliminary
March 2011
Revision 1.1

NPCT42x Trusted Platform Module (TPM)

General Description

The NPCT42x single-chip Trusted Platform Module (TPM) is a family of third-generation, Nuvoton Softkeyper™ technology devices. The devices implement the Trusted Computing Group (TCG) version 1.2 specifications for PC-Client TPM.

The NPCT42x devices are designed to reduce system boot time and Trusted OS loading time. They provide a solution for PC security for a wide range of PC applications.

The NPCT42x family of devices are Microsoft® Windows® compliant and are supported by Linux kernel v2.6.18 and higher.

Features

General

- [illegible] up TPM solution
- [illegible] No required
- [illegible] ication Version 1.2
- [illegible] TPM interface
- Host Interface
  - TPM 1.2 standard interface (I²C)
  - Supports legacy locality for using TIS protocol I/O-mapped registers
- Secure General-Purpose I/O (GPIO)
  - Four GPIO pins
  - I/O pins individually configured as input or output
  - Configurable internal pull-up resistors
  - TCG 1.2-defined interface
  - Dedicated Physical Presence (PP) pin with configurable pull-up or pull-down resistor
- Two Counter

Bus Interface

- LPC Bus Interface
  - Based on Intel's LPC Interface Specification Revision 1.1, August 2006
  - TPM 1.2i interface (TIS)

Clocking and Supply

- On-Chip Clock Generator
- Power Supply
  - 3.3V supply operation
  - Separate pins for main (V_{DD}) and standby (V_{SB}) power supplies
  - Low standby power consumption

Software

- TPM BIOS: Advanced Memory Aperture (AMA) and Memory Present (MP)
- TPM Device Driver for Microsoft Windows
- [illegible] Cryptographic Library for Security Infrastructure [illegible]
- [illegible] Key Storage Provider (CSP)
- [illegible] Platform [illegible]

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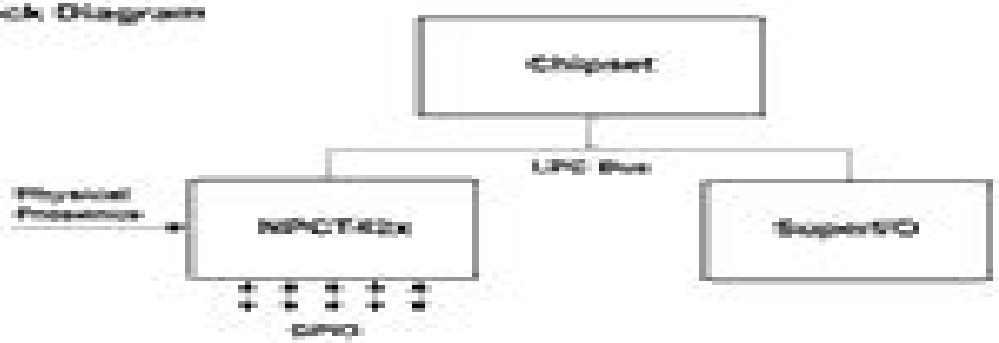
System Block Diagram



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Travis Goodspeed

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