

Seat No.	
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S.Y.B.Sc. (Computer Science) (Semester – I) Examination, 2013
ELECTRONICS (Paper – I)
ELC – 211 : Microprocessor Architecture and Programming
(2008 Pattern)

Time : 2 Hours

Max. Marks : 40

- Instructions :** 1) *All questions are compulsory.*
 2) *Figures to the right indicate full marks.*
 3) *Neat diagram must be drawn whenever necessary.*

1. Answer the following questions in **one** or **two** sentences : **(10×1=10)**
- a) In memory stack where is the stack pointer initialised ?
 - b) Which memories can be directly accessed by the CPU ?
 - c) Why two floating point instructions are not possible at the same time ?
 - d) What will be the result of XOR CX, CX ?
 - e) What is a device driver ?
 - f) Give the location and size of IVT.
 - g) State any two advantages of a flow chart.
 - h) Identify the type of instruction for the following :
 - i) DAA
 - ii) CMC
 - i) What will be the contents of AL & BL register after executing the following ?
 MOV AL, 62 H
 MOV BL, AL
 SHR AL, 4
 AND BL, 0FH
 - j) If AH = 08 H and INT 21 H is executed, what is the processor going to do ?

Microprocessor Question Papers

Mike Jess



Microprocessor Question Papers:

Digital Systems & Microprocessors: Objective Question Bank for North American Graduates Chandresh Agrawal, Nandini Books, 2026-03-06 Ebook Details Primary Title Digital Systems Study Aids Professional Licensure Ebook Description Master the Core of Computer Engineering through Focused Practice For engineering graduates in the USA and Canada a deep understanding of digital logic and microprocessor fundamentals is essential for both the FE Exam and professional technical interviews Digital Systems Microprocessors provides a high volume question bank designed to reinforce these critical concepts What is Covered This volume focuses on the hardware and low level software interface Digital Logic Design Logic gates Boolean minimization K maps and combinational circuit analysis Microprocessor Fundamentals Extensive practice on the 8085 architecture which serves as the academic standard for learning instruction sets and timing Memory I O Interfacing Questions on addressing modes memory mapping and peripheral interfacing Interrupts Control Detailed sets on interrupt structures stack operations and timing diagrams What is NOT Covered High Level Programming This book does not cover C Python or Java It is strictly focused on assembly level architecture and hardware Advanced VHDL Verilog While logic design is covered hardware description languages are not the focus Step by Step Explanations To provide a high volume of questions at an affordable price this book provides the Answer Key but does not include lengthy theoretical derivations The Instant Book Concept Order Download Print We believe that complex logic diagrams and timing charts are best studied on paper This ebook is optimized for our Instant Book Concept Immediate Download Purchase and receive your 550 page PDF instantly No waiting for 7 day shipping within the US or Canada Professional Print Ready Formatted for high quality printing on standard Letter 8 5 x 11 or A4 paper The Spiral Bound Advantage We strongly recommend printing the file and having it spiral bound at a local shop like FedEx Office or Staples A spiral bound book lies flat on your desk allowing you to trace logic paths and solve Boolean equations directly on the page a far superior experience to a digital screen Modular Revision Print individual practice sets to take to your study group or library keeping your preparation organized and portable Trademark Content Disclaimer Disclaimer to Candidates NCEES and FE are registered trademarks of the National Council of Examiners for Engineering and Surveying This publication is an independent study resource authored by Dr Chandresh Agrawal and is not affiliated with endorsed by or sponsored by NCEES The questions provided are original practice sets designed to align with graduate level engineering standards and are not reproduced from actual past exam papers provided by the council

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