

## Chapter # 4: Programmable and Steering Logic

*Contemporary Logic Design*

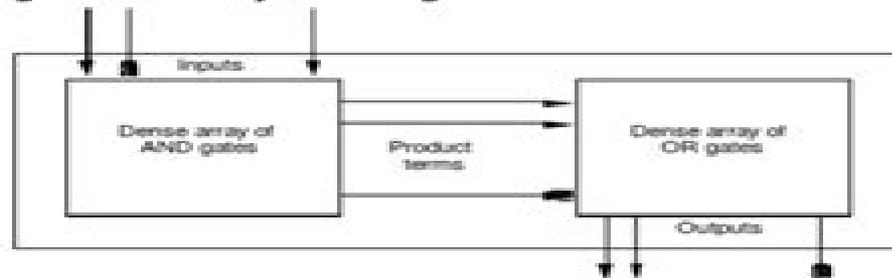
Randy H. Katz  
University of California, Berkeley

June 1993

### PALs and PLAs

Pre-fabricated building block of many AND/OR gates (or NOR, NAND) "Personalized" by making or breaking connections among the gates

*Programmable Array Block Diagram for Sum of Products Form*



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**CO Houle**



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