

### Circuit Analysis

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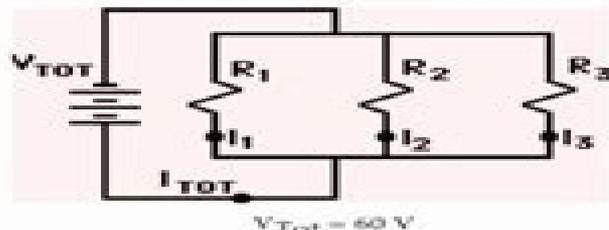
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MOP Connection: Electric Circuits: sublevel 11

1. Fill in the blanks in the following diagram. Show appropriate units.



$R_1 = 12.5 \ \Omega$      $R_2 = 14.7 \ \Omega$      $R_3 = 19.1 \ \Omega$

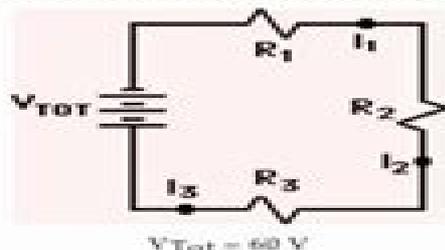
$R_{Tot} =$  \_\_\_\_\_     $I_{Tot} =$  \_\_\_\_\_

$\Delta V_1 =$  \_\_\_\_\_     $I_1 =$  \_\_\_\_\_

$\Delta V_2 =$  \_\_\_\_\_     $I_2 =$  \_\_\_\_\_

$\Delta V_3 =$  \_\_\_\_\_     $I_3 =$  \_\_\_\_\_

2. Fill in the blanks in the following diagram. Show appropriate units.



$R_1 = 12.5 \ \Omega$      $R_2 = 14.7 \ \Omega$      $R_3 = 19.1 \ \Omega$

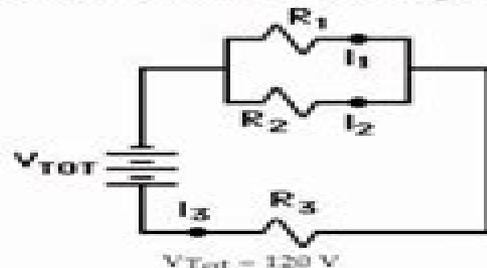
$R_{Tot} =$  \_\_\_\_\_     $I_{Tot} =$  \_\_\_\_\_

$\Delta V_1 =$  \_\_\_\_\_     $I_1 =$  \_\_\_\_\_

$\Delta V_2 =$  \_\_\_\_\_     $I_2 =$  \_\_\_\_\_

$\Delta V_3 =$  \_\_\_\_\_     $I_3 =$  \_\_\_\_\_

3. Fill in the blanks in the following diagram. Show appropriate units.



$R_1 = 16.0 \ \Omega$      $R_2 = 16.0 \ \Omega$      $R_3 = 6.9 \ \Omega$

$R_{Tot} =$  \_\_\_\_\_     $I_{Tot} =$  \_\_\_\_\_

$\Delta V_1 =$  \_\_\_\_\_     $I_1 =$  \_\_\_\_\_

$\Delta V_2 =$  \_\_\_\_\_     $I_2 =$  \_\_\_\_\_

$\Delta V_3 =$  \_\_\_\_\_     $I_3 =$  \_\_\_\_\_

# Combination Circuits Practice Problems With Answers

**NATARAJAN, R. ANANDA**



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**DC/AC Electrical Fundamentals** Dale R. Patrick, Stephen W. Fardo, Ray Richardson, Vigyan (Vigs) Chandra, 2024-02-09  
This book explores many essential topics in a basic and easy to understand manner. This book and the accompanying Electronic Devices and Circuit Fundamentals have been modified with significant updates in content. The books are developed using a classic textbook Electricity and Electronics A Survey 5th Edition as a framework. Both new books have been structured using a similar sequence and organization as previous editions. The previous edition of Electricity and Electronics A Survey contained 18 chapters: 8 in the Electricity section and 10 in the Electronics section. This book has been expanded to include 19 chapters, further simplifying content and providing a more comprehensive coverage of the content. The content has been continually updated and revised through new editions and by reviewers over the years. Additional quality checks to ensure technical accuracy, clarity, and coverage of content have always been an area of focus. Each edition of the text has been improved through the following features: Improved and updated text content; Improved usage of illustrations and photos; Use of color to add emphasis and clarify content.

**Digital Electronics** GATE, PSUS AND ES Examination Satish K Karna, Test Prep for Digital Electronics GATE PSUS AND ES Examination

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**Questions and Answers about Electrical Apparatus** William Brasher Clayton, James Watt Craig, 1909

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Algebra Fourier Series Circuits and Networks which helps engineering students understand the subject better and build a concrete foundation of their concepts      **Examples in Magnetism** Frank Eugene Austin,1916      **The American Exchange and Review** ,1892      **Essentials of Electricity-electronics** Morris Slurzberg,William Osterheld,1965      **Telegraph and Telephone Age** ,1910      DIGITAL DESIGN NATARAJAN, R. ANANDA,2015-01-17 Primarily intended for undergraduate engineering students of Electronics and Communication Electronics and Electrical Electronics and Instrumentation Computer Science and Information Technology this book will also be useful for the students of BCA B Sc Electronics and CS M Sc Electronics and CS and MCA Digital Design is a student friendly textbook for learning digital electronic fundamentals and digital circuit design It is suitable for both traditional design of digital circuits and HDL based digital design This well organised text gives a comprehensive view of Boolean logic logic gates and combinational circuits synchronous and asynchronous circuits memory devices semiconductor devices and PLDs and HDL VHDL and Verilog programming Numerous solved examples are given right after conceptual discussion to provide better comprehension of the subject matter VHDL programs along with simulation results are given for better understanding of VHDL programming Key features Well labelled illustrations provide practical understanding of the concepts GATE level MCQs with answers along with detailed explanation wherever required at the end of each chapter help students to prepare for competitive examinations Short questions with answers and appropriate number of review questions at the end of each chapter are useful for the students to prepare for university exams and competitive exams Separate chapters on VHDL and Verilog programming along with simulated results are included to enhance the programming skills of HDL

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