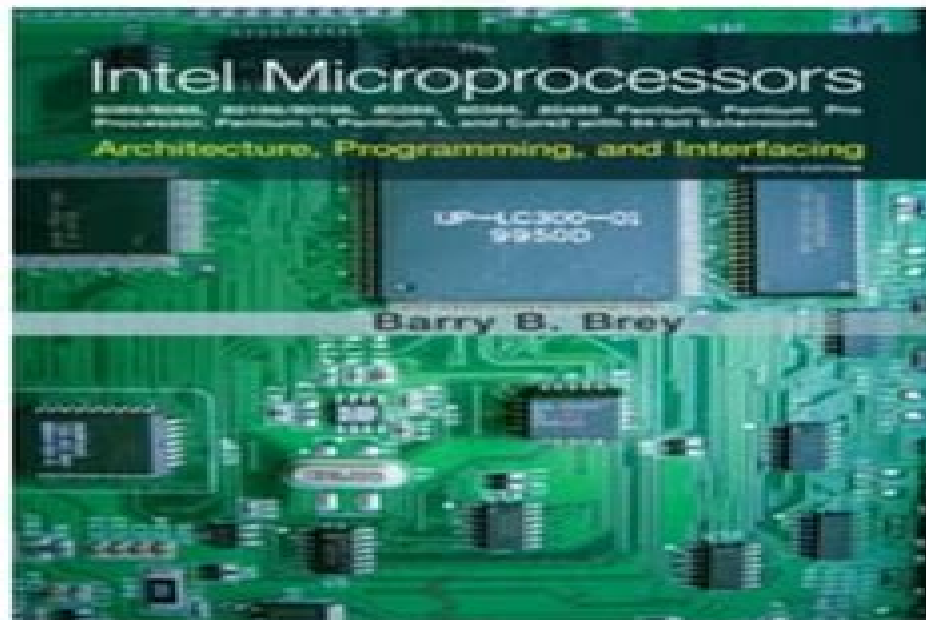


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**Artificial Plasmonics for VLSI Interconnects** Soumitra R. Joy, Pinaki Mazumder, 2025-12-16 Build the microchips of the future with this revolutionary new information transfer technology Advancements in high performance computing have continually demanded for progresses in disruptive technological research and innovations Moore's Law has pushed the Very Large Scale Integration VLSI technology to pack MOS devices inside a chip at an exponential rate thereby surpassing now eight billion transistors per cm<sup>2</sup> This has concomitantly fueled the growth of multilayered on chip interconnects comprising metallic and low dielectric materials VLSI Interconnect Technology with Artificial Plasmonics introduces a new method for improving chip performance by harnessing the power of information transfer among chips at terahertz frequency This revolutionary new electromagnetic wave engineering called a spoof surface plasmon polariton adapts the principles of VLSI and terahertz interconnect technology along with the artificial plasmonics to transfer huge quantities of data at vastly improved speeds It constitutes a potentially decisive contribution to the pursuit of faster and more capacious VLSI chips In VLSI Interconnect Technology with Artificial Plasmonics readers will also find A cutting edge new approach supported by pioneering research Detailed discussion of essential components related to the development of THz interconnect technology including theory modeling simulation and validation Roadmap to future technological development in the branch of artificial plasmonics VLSI Interconnect Technology with Artificial Plasmonics is ideal for engineers researchers and scientists working in electronics electromagnetics and optics Colour renditions of the images included in the book are available here <https://www.wiley.com/go/artificialplasmonics>

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## **Table of Contents Intel Microprocessors Barry B Brey 8th Edition**

1. Understanding the eBook Intel Microprocessors Barry B Brey 8th Edition
  - The Rise of Digital Reading Intel Microprocessors Barry B Brey 8th Edition
  - Advantages of eBooks Over Traditional Books
2. Identifying Intel Microprocessors Barry B Brey 8th Edition
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Intel Microprocessors Barry B Brey 8th Edition
  - User-Friendly Interface
4. Exploring eBook Recommendations from Intel Microprocessors Barry B Brey 8th Edition
  - Personalized Recommendations
  - Intel Microprocessors Barry B Brey 8th Edition User Reviews and Ratings
  - Intel Microprocessors Barry B Brey 8th Edition and Bestseller Lists
5. Accessing Intel Microprocessors Barry B Brey 8th Edition Free and Paid eBooks
  - Intel Microprocessors Barry B Brey 8th Edition Public Domain eBooks
  - Intel Microprocessors Barry B Brey 8th Edition eBook Subscription Services
  - Intel Microprocessors Barry B Brey 8th Edition Budget-Friendly Options
6. Navigating Intel Microprocessors Barry B Brey 8th Edition eBook Formats

- ePub, PDF, MOBI, and More
  - Intel Microprocessors Barry B Brey 8th Edition Compatibility with Devices
  - Intel Microprocessors Barry B Brey 8th Edition Enhanced eBook Features
7. Enhancing Your Reading Experience
    - Adjustable Fonts and Text Sizes of Intel Microprocessors Barry B Brey 8th Edition
    - Highlighting and Note-Taking Intel Microprocessors Barry B Brey 8th Edition
    - Interactive Elements Intel Microprocessors Barry B Brey 8th Edition
  8. Staying Engaged with Intel Microprocessors Barry B Brey 8th Edition
    - Joining Online Reading Communities
    - Participating in Virtual Book Clubs
    - Following Authors and Publishers Intel Microprocessors Barry B Brey 8th Edition
  9. Balancing eBooks and Physical Books Intel Microprocessors Barry B Brey 8th Edition
    - Benefits of a Digital Library
    - Creating a Diverse Reading Collection Intel Microprocessors Barry B Brey 8th Edition
  10. Overcoming Reading Challenges
    - Dealing with Digital Eye Strain
    - Minimizing Distractions
    - Managing Screen Time
  11. Cultivating a Reading Routine Intel Microprocessors Barry B Brey 8th Edition
    - Setting Reading Goals Intel Microprocessors Barry B Brey 8th Edition
    - Carving Out Dedicated Reading Time
  12. Sourcing Reliable Information of Intel Microprocessors Barry B Brey 8th Edition
    - Fact-Checking eBook Content of Intel Microprocessors Barry B Brey 8th Edition
    - Distinguishing Credible Sources
  13. Promoting Lifelong Learning
    - Utilizing eBooks for Skill Development
    - Exploring Educational eBooks
  14. Embracing eBook Trends
    - Integration of Multimedia Elements
    - Interactive and Gamified eBooks

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