

10. The 80286 is able to address the physical memory of

- (a) 8 MB (b) 16 MB
(c) 24 MB (d) 64 MB

SECTION B — (5 × 5 = 25 marks)

1. (a) Convert the following decimal numbers to the bases indicated.

- (i) 7562 to octal
(ii) 1938 to hexadecimal
(iii) 175 to binary

Or

(b) Discuss about the parallel binary adder with suitable diagram.

(a) Explain the Don't care conditions with example.

Or

(b) Explain the functioning of a Demultiplexer with diagram.

(a) Explain the working principle of asynchronous data transfer.

Or

(b) Distinguish between isolated I/O and memory mapped I/O.

14. (a) Explain main memory in detail.

Or

(b) Discuss the cache memory concept diagram. What is its need?

15. (a) Explain the architecture of 80286.

Or

(b) Explain the basic features of 80486.

SECTION C — (5 × 8 = 40 marks)

16. (a) Why NAND gate is called as an inverting gate? Explain with example.

Or

(b) Explain Full and Half subtractor with diagram.

17. (a) Minimize four variables Boolean using K-map method.

Or

(b) Describe the design of the synchronous counter and explain with truth table.

18. (a) Explain Priority interrupt in detail.

Or

(b) Discuss on DMA controller.

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Guillaume Favre



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Table of Contents Bharathiyar University Question Paper

1. Understanding the eBook Bharathiyar University Question Paper
 - The Rise of Digital Reading Bharathiyar University Question Paper
 - Advantages of eBooks Over Traditional Books
2. Identifying Bharathiyar University Question Paper
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Bharathiyar University Question Paper
 - User-Friendly Interface
4. Exploring eBook Recommendations from Bharathiyar University Question Paper
 - Personalized Recommendations
 - Bharathiyar University Question Paper User Reviews and Ratings
 - Bharathiyar University Question Paper and Bestseller Lists
5. Accessing Bharathiyar University Question Paper Free and Paid eBooks
 - Bharathiyar University Question Paper Public Domain eBooks
 - Bharathiyar University Question Paper eBook Subscription Services

- Bharathiyar University Question Paper Budget-Friendly Options
- 6. Navigating Bharathiyar University Question Paper eBook Formats
 - ePub, PDF, MOBI, and More
 - Bharathiyar University Question Paper Compatibility with Devices
 - Bharathiyar University Question Paper Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Bharathiyar University Question Paper
 - Highlighting and Note-Taking Bharathiyar University Question Paper
 - Interactive Elements Bharathiyar University Question Paper
- 8. Staying Engaged with Bharathiyar University Question Paper
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Bharathiyar University Question Paper
- 9. Balancing eBooks and Physical Books Bharathiyar University Question Paper
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Bharathiyar University Question Paper
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine Bharathiyar University Question Paper
 - Setting Reading Goals Bharathiyar University Question Paper
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Bharathiyar University Question Paper
 - Fact-Checking eBook Content of Bharathiyar University Question Paper
 - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
- 14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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