

# **Implementation of Image Compression Algorithm using Verilog with Area, Power and Timing Constraints**

**A THESIS SUBMITTED IN PARTIAL FULFILLMENT  
OF THE REQUIREMENTS FOR THE DEGREE OF**

**Master of Technology**

**in**

**VLSI Design and Embedded System**

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**2007-2009**

# Implementation Of Image Compression Algorithm Using

**Rosina Ehmman**



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