

SECOND EDITION

DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE

JAN M. RABAEY
ANANTHA CHANDRAKASAN
BORIVOJE NIKOLIĆ

Unique in approach, this text offers comprehensive coverage of new design methodologies and implementation strategies of digital ICs and brings to the forefront a number of new issues that impact the reliability, cost, performance, and power dissipation of chips now reaching the 100-nm realm. This means increased importance in circuit design of deep submicron transistor effects, interconnect, signal integrity, high-performance and low-power design, timing and clock distribution. In contrast to the first edition, the present text focuses entirely on CMOS ICs. While introducing these changes, the text preserves the spirit and goals of the first edition—that is, it successfully bridges the gap between the circuit and system visions on digital design.

Beginning with solid discussions on the operation of electronic devices and in-depth analysis of the nucleus of digital design—the inverter—the text gradually channels this knowledge into the design of more complex modules such as gates, controllers, adders, multipliers, and memories. Actual design examples and layouts are interspersed throughout the text. Besides numerous examples, each chapter also contains a number of problems or brain teasers that provoke thinking and understanding while reading. Fully worked-out design problems are available from the web site: (<http://bwrc.eecs.berkeley.edu/icBook>).

The text is intended for use in digital circuit design courses conducted for undergraduate and postgraduate students of electronics.



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Solutions Manual to Accompany Analysis and Design of Digital Integrated Circuits David A. Hodges, Yu Chen, Horace G. Jackson, 1983 *Solutions Manual Digital Integrated Circuits* CRC Press, 2009-07-10 Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field. Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization. Each chapter includes numerous worked examples, case studies, and SPICE computer simulations. The book's website offers supplementary material and more worked problems. Qualifying instructors will have access to a new instructor's manual. **Digital Integrated Circuits** DeMassa, 1996-02-01

Solutions Manual for Digital Integrated Circuits Ayers John E, 2003-09 **Embedded SoPC Design with Nios II Processor and Verilog Examples** Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog. An SoPC system on a programmable chip integrates a processor, memory, modules, I/O peripherals, and custom hardware accelerators into a single FPGA field programmable gate array device. In addition to the customized software, customized hardware can be developed and incorporated into the embedded system as well, allowing us to configure the soft core processor, create tailored I/O interfaces, and develop specialized hardware accelerators for computation intensive tasks. Utilizing an Altera FPGA prototyping board and its Nios II soft core processor, *Embedded SoPC Design with Nios II Processor and Verilog Examples* takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board. Emphasizing hardware design and integration throughout, the book is divided into four major parts. Part I covers HDL and synthesis of custom hardware. Part II introduces the Nios II processor and provides an overview of embedded software development. Part III demonstrates the design and development of hardware and software of several complex I/O peripherals, including a PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD secure digital card. Part IV provides several case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. While designing and developing an embedded SoPC can be rewarding, the learning can be a long and winding journey. This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology. **Embedded SoPC Design with Nios II Processor and VHDL Examples** Pong P. Chu, 2011-08-29 The book is divided into four major parts. Part I covers HDL constructs and synthesis of basic digital circuits. Part II provides an overview of embedded software development with the emphasis on low level I/O access and drivers. Part III demonstrates the design and development of

hardware and software for several complex I/O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I/O configuration

Digital Integrated Circuits John E.

Ayers,2018-09-03 Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years The second edition of Digital Integrated Circuits Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication this book delves into the dramatic advances in the field including new applications and changes in the physics of operation made possible by relentless miniaturization This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic Like the first edition this volume is a crucial link for integrated circuit engineers and those studying the field supplying the cross disciplinary connections they require for guidance in more advanced work For pedagogical reasons the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models With four new chapters more than 200 new illustrations numerous worked examples case studies and support provided on a dynamic website this text significantly expands concepts presented in the first edition

FPGA Prototyping by VHDL Examples

Pong P. Chu,2011-09-20 This book uses a learn by doing approach to introduce the concepts and techniques of VHDL and FPGA to designers through a series of hands on experiments FPGA Prototyping by VHDL Examples provides a collection of clear easy to follow templates for quick code development a large number of practical examples to illustrate and reinforce the concepts and design techniques realistic projects that can be implemented and tested on a Xilinx prototyping board and a thorough exploration of the Xilinx PicoBlaze soft core microcontroller

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Design and Test Technology for Dependable Systems-on-Chip Ubar, Raimund, Raik, Jaan, Vierhaus, Heinrich Theodor, 2010-12-31 This book covers aspects of system design and efficient modelling and also introduces various fault models and fault mechanisms associated with digital circuits integrated into System on Chip SoC Multi Processor System on Chip MPSoC or Network on Chip NoC Engineering Education ,1977 Scientific and Technical Books and Serials in Print ,1989 **Single-chip Bluetooth Solutions** Sudepto Chakraborty, 2001 **Books in Series** ,1985 Vols for 1980 issued in three parts Series Authors and Titles Forthcoming Books Rose Arny, 2003 *Solutions Manual to Accompany Digital Concepts Using Standard Integrated Circuits* Richard S. Sandige, 1978 **The Cumulative Book Index** ,1997 A world list of books in the English language *Subject Guide to Forthcoming Books* ,1983 Presents by subject the same titles that are listed by author and title in Forthcoming books **Bibliographic Index** ,1997 *Proceedings of MELECON* ,2002

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hebrew bible balaam and the angel 1836 by gustav jäger the angel in this incident is referred to as a satan 6 the hebrew term šāṭān hebrew שָׂטָן is a generic noun meaning accuser or adversary 7 8 and is derived from a verb meaning primarily to obstruct oppose 9

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ryan satin is an american pop culture and professional wrestling journalist he was a producer for tmz 1 and founded pro wrestling sheet 2 which reports on professional wrestling and mixed martial arts he has since left the site and now works for fox sports he is the son of scott satin a hollywood producer and writer 3 career

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feb 1 2018 alan whiticker is the co author of the satin man which first revealed harry phipps as a possible suspect mr whiticker believes harry phipps gained the children s trust by giving them a one pound note before luring them to his house which was just hundreds of metres away

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