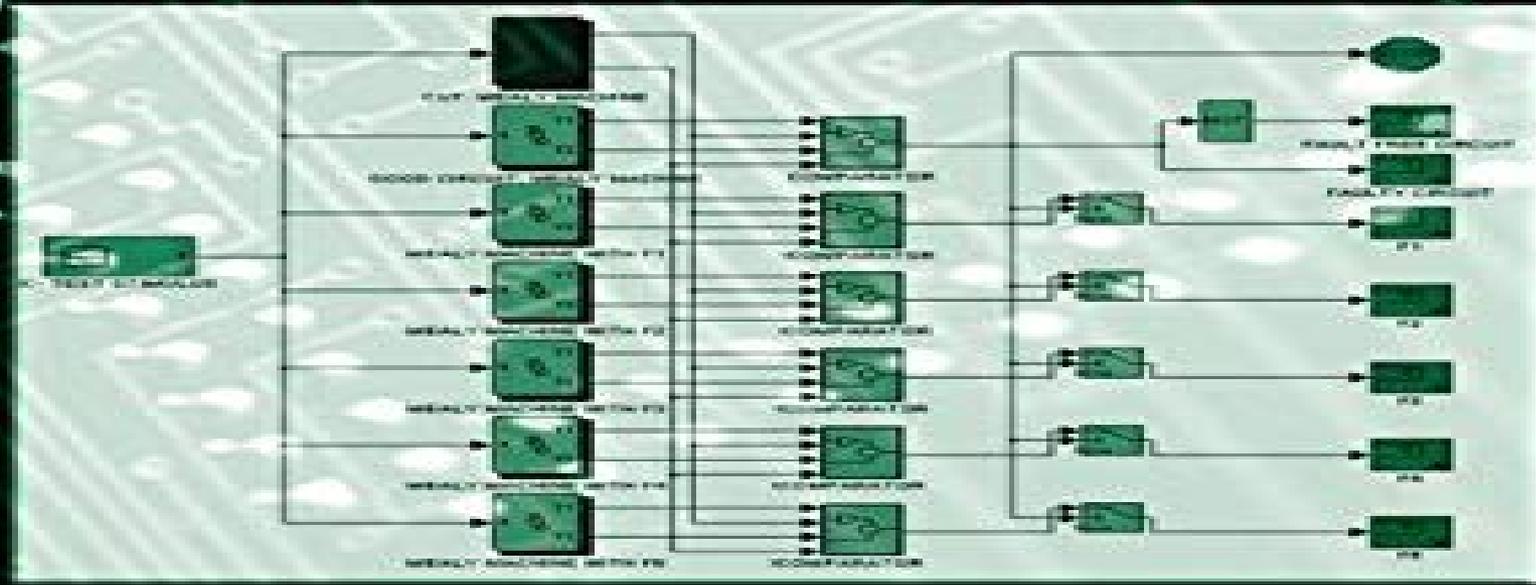


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Digital Integrated Circuits Design For Test Using Simulink And Stateflow

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