

UNIT-I
SINGLE – STAGE MOS and BJT AMPLIFIERS

Learning Outcomes

Upon successful completion of the course, the students will be able to :

CO1: Analyze and characterize the MOS and BJT single stage amplifiers at low and high frequencies.

Syllabus:

UNIT- I: Single-Stage MOS and BJT Amplifiers

Single-Stage MOS amplifiers: Three basic configurations, Characterizing Amplifiers, The common-source (CS) amplifier.

Single-Stage BJT amplifiers: Basic Structure, The common-Emitter (CE) amplifier.

High Frequency response: General considerations, High-frequency response of CS and CE amplifier.

1.1 Three basic Configurations

1. In an amplifier, any increase in ac voltage, current, or power is the result of a transfer of energy from the _____ [BL2]

- a) resistor
- b) applied dc supply
- c) ground
- d) None of the above

2. When performing DC analysis in a circuit make capacitors in the circuit _____ [BL2]

- a) short circuit
- b) open circuit
- c) input side short and output side open
- d) None of the above

3. The basic relationship between input and output currents of a BJT in Common Emitter configuration is _____ [BL2]

- a) $I_b = \gamma I_c$
- b) $I_c = \alpha I_b$

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**Esteban Tlelo-Cuautle, Mourad
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Analog Circuits Esteban Tlelo-Cuautle, Mourad Fakhfakh, Luis Gerardo de la Fraga, 2017 This book includes recent research that focuses on analog integrated circuits and covers three main topics namely fundamentals synthesis and performance Eleven chapters are divided among these three topics as follows Chapters One to Four are a part of fundamentals The first chapter The Next Generation of Nanomaterials for Designing Analog Integrated Circuits describes new directions for applying nanomaterials for the design of modern analog circuits Chapter Two Application of Nullors in Designing Analog Circuits for Frequency Bandwidth uses the pathological circuit element known as a nullor to design analog integrated circuits with frequency specifications to accomplish a desired bandwidth Chapter Three RC and RL to LC Circuit Conversion and its Application in Poles and Zeros Identification details an important property from circuit theory to estimate

roots by performing conversions of passive elements Chapter Four Enhanced and Improved Symbolic Circuit Analysis Using MATLAB relays the development of symbolic circuit analysis and focuses on enhancing an already developed symbolic tool to allow the symbolic analysis of large circuits The synthesis of analog integrated circuits has been a challenge because there is no way to establish general rules to cover the gap between the behavioral and transistor circuit levels of abstraction In this book the second topic includes four chapters from Five to Eight Chapter Five On the Synthesis of Sinusoidal Oscillators Using Nullors just as in Chapter Two uses the pathological circuit element known as a nullor to perform the synthesis of sinusoidal oscillators which are quite useful in many electronic systems Other kinds of oscillators are described in Chapter Six Synthesis of SRCOs and Multi Phase Oscillators from State Variables to their Implementation Using CMOS IC Technology where the synthesis process identifies the resistor that controls the oscillating frequency and applies a state variable approach Chapter Seven Evolutionary Optimisation in the Design of CMOS Analog Integrated Circuits shows the application of heuristics for circuit optimisation and how it can be extended to bigger analog integrated circuits Chapter Eight provides details on the synthesis and design of a CMOS harmonic mixer with output power management for narrowband and wideband wireless communications the Bluetooth and UWB cases The third part of this book is devoted to analog circuit performances and includes three chapters Chapter Nine details the FPGA realisation of radio frequency RF power amplifier models In this case the system is modeled in the analog domain and implemented in the digital one Chapter Ten White Box Models of Optimal Sized Solutions of Analog Integrated Circuits generates analytical expressions for modeling the dominant behavior of CMOS analog circuits Finally Chapter Eleven Radial Basis Function Surrogate Modeling for the Accurate Design of Analog Circuits applies modern modeling approaches to accomplish real target specifications and to improve the design of reliable circuits

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subject on your own read for pleasure or completed coursework it will assess your knowledge and prepare you for competitive exams quizzes trivia and more

Analog Circuits and Systems Optimization based on Evolutionary Computation Techniques Manuel Barros, Jorge Guilherme, Nuno Horta, 2010-04-13 The microelectronics market with special emphasis to the production of complex mixed signal systems on chip SoC is driven by three main dynamics time market productivity and managing complexity Pushed by the progress in nanometer technology the design teams are facing a curve of complexity that grows exponentially thereby slowing down the productivity design rate Analog design automation tools are not developing at the same pace of technology once custom design characterized by decisions taken at each step of the analog design flow lies most of the time on designer knowledge and expertise Actually the use of sign management platforms like the Cadences Virtuoso platform with a set of tegrated CAD tools and database facilities to deal with the design transformations from the system level to the physical implementation can significantly speed up the design process and enhance the productivity of analog mixed signal integrated circuit IC design teams These design management platforms are a valuable help in analog IC design but they are still far behind the development stage of design automation tools already available for digital design Therefore the development of new CAD tools and design methodologies for analog and mixed signal ICs is ess tial to increase the designer s productivity and reduce design productivitygap The work presented in this book describes a new design automation approach to the problem of sizing analog ICs

Analog Electronics Ian Hickman, 1999-06-10 Analog Electronics is a vital book for all electronics designers to have to hand it will answer nagging questions about core analog theory and design principles as well as offering practical design ideas The second edition of this popular text has been enhanced with concise design implementations with many of the circuits taken from Ian Hickman s magazine articles Although not a traditional textbook Analog Electronics is also an ideal course text for students at HNC HND and degree level The contents have been carefully matched to provide full coverage of the appropriate units in the new BTEC Higher National Engineering scheme from Edexcel Ian Hickman is looked to by thousands of circuit designers for his innovative design ideas and clear explanations of the fundamentals of analog circuit design This book is a distillation of Hickman s design insights introducing all the main areas of analog electronics The professional text for analog electronics Includes numerous practical circuit ideas

Analog Circuits ,1979 *Analog Circuit Design* Jim Williams, 1991-06-19 This book is far more than just another tutorial or reference guide it s a tour through the world of analog design combining theory and applications with the philosophies behind the design process Readers will learn how leading analog circuit designers approach problems and how they think about solutions to those problems They ll also learn about the analog way a broad flexible method of thinking about analog design tasks A comprehensive and useful guide to analog theory and applications Covers visualizing the operation of analog circuits Looks at how to rapidly determine workable approximations of analog circuit parameters

Advances in Analog Circuits Esteban Tlelo-Cuautle, 2011-02-02 This book highlights key design issues and challenges

to guarantee the development of successful applications of analog circuits Researchers around the world share acquired experience and insights to develop advances in analog circuit design modeling and simulation The key contributions of the sixteen chapters focus on recent advances in analog circuits to accomplish academic or industrial target specifications

Analog Circuits and Devices Wai-Kai Chen, 2003-03-26 The Principles and Application in Engineering Series is a new series of convenient economical references sharply focused on particular engineering topics and subspecialties Each volume in this series comprises chapters carefully selected from CRC's bestselling handbooks logically organized for optimum convenience and thoughtfully priced to fit

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Fault Diagnosis of Analog Integrated Circuits Prithviraj Kabisatpathy, Alok Barua, Satyabroto Sinha, 2006-01-13 System on Chip SOC having both digital and analog circuits has become increasingly prevalent in integrated circuit manufacturing industry Electronic tests are classified as digital analog and mixed signal Current methodologies for the testing of digital circuits are well developed In contrast methodologies for the testing of analog circuits remain relatively underdeveloped due to the complex nature of analog signals Compared to digital testing analog testing lags far behind in methodologies and tools and therefore demands substantial research and development effort Fault Diagnosis of Analog Integrated Circuits is a textbook for advanced undergraduate and graduate level students as well as practicing engineers The objective of this book is to study the testing and fault diagnosis of

analog and analog part of mixed signal circuits A background in analog integrated circuit artificial neural network is desirable but not essential The text covers the testing and fault diagnosis of both bipolar and Metal Oxide Semiconductor MOS circuits Fault model of the devices in analog domain has been introduced in the text The test stimulus generations are also discussed in details Experimental verification of some state of the art techniques has also been presented in the book It also contains problems that can be used as quiz or homework This book enables the reader to test an analog circuit that is implemented either in bipolar or MOS technology

Hickman's Analog and RF Circuits Ian Hickman, 1998-01-15 Hickman's latest guide is essential reading for anyone designing analog circuits This book along with the recent *Analog Circuits Cookbook* also available from Newnes will enlighten inform interest and even amuse readers and give them the ability to tackle analog and RF design problems with confidence Based on articles published in *Electronics World* this book covers such topics as RF amplifiers oscillator design and behaviour waveform analysis optoelectronics filters and op amps as well as offering intriguing insights in chapters such as *Cautionary Tales for Circuit Designers* *Circuit Reflections* and *Is Matching Easy* Ian Hickman is one of the world's leading analog and RF engineers Using illustrations and examples rather than tough mathematical theory Ian Hickman presents a wealth of ideas and tips based on his own workbench experience Essential reading for analog circuit designers Hickman's wit and wisdom is based on a wealth of industrial experience Helps readers tackle analog and RF design problems with confidence

Practical Synthesis of High-Performance Analog Circuits Emil S. Ochotta, Tamal Mukherjee, Rob A. Rutenbar, L. Richard Carley, 2012-12-06 *Practical Synthesis of High Performance Analog Circuits* presents a technique for automating the design of analog circuits Market competition and the astounding pace of technological innovation exert tremendous pressure on circuit design engineers to turn ideas into products quickly and get them to market In digital Application Specific Integrated Circuit ASIC design computer aided design CAD tools have substantially eased this pressure by automating many of the laborious steps in the design process thereby allowing the designer to maximise his design expertise But the world is not solely digital Cellular telephones magnetic disk drives neural networks and speech recognition systems are a few of the recent technological innovations that rely on a core of analog circuitry and exploit the density and performance of mixed analog digital ASICs To maximize profit these mixed signal ASICs must also make it to market as quickly as possible However although the engineer working on the digital portion of the ASIC can rely on sophisticated CAD tools to automate much of the design process there is little help for the engineer working on the analog portion of the chip With the exception of simulators to verify the circuit design when it is complete there are almost no general purpose CAD tools that an analog design engineer can take advantage of to automate the analog design flow and reduce his time to market *Practical Synthesis of High Performance Analog Circuits* presents a new variation tolerant analog synthesis strategy that is a significant step towards ending the wait for a practical analog synthesis tool A new synthesis strategy is presented that can fully automate the path from a circuit topology and performance specifications to a

sized variation tolerant circuit schematic This strategy relies on asymptotic waveform evaluation to predict circuit performance and simulated annealing to solve a novel non linear infinite programming optimization formulation of the circuit synthesis problem via a sequence of smaller optimization problems Practical Synthesis of High Performance Analog Circuits will be of interest to analog circuit designers CAD EDA industry professionals academics and students

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 - The Rise of Digital Reading Analog Circuits Objective Questions Answers Pdf Download
 - Advantages of eBooks Over Traditional Books
2. Identifying Analog Circuits Objective Questions Answers Pdf Download
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Analog Circuits Objective Questions Answers Pdf Download
 - User-Friendly Interface
4. Exploring eBook Recommendations from Analog Circuits Objective Questions Answers Pdf Download
 - Personalized Recommendations
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 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Analog Circuits Objective Questions Answers Pdf Download
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 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Analog Circuits Objective Questions Answers Pdf Download
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 - Minimizing Distractions
 - Managing Screen Time
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