

Jurij Šilc
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Processor Architecture

From Dataflow to Superscalar and Beyond



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DJ Losen



Processor Architecture From Dataflow To Superscalar And Beyond:

Processor Architecture Jurij Silc, Borut Robic, Theo Ungerer, 2012-12-06 Today's microprocessors are the powerful descendants of the von Neumann computer dating back to a memo of Burks Goldstine and von Neumann of 1946. The so-called von Neumann architecture is characterized by a sequential control flow resulting in a sequential instruction stream. A program counter addresses the next instruction if the preceding instruction is not a control instruction such as e.g. jump, branch, subprogram call, or return. An instruction is coded in an instruction format of fixed or variable length where the opcode is followed by one or more operands that can be data addresses of data or the address of an instruction in the case of a control instruction. The opcode defines the types of operands. Code and data are stored in a common storage that is linearly addressed in units of memory words (bytes, words, etc.). The overwhelming design criterion of the von Neumann computer was the minimization of hardware and especially of storage. The most simple implementation of a von Neumann computer is characterized by a microarchitecture that defines a closely coupled control and arithmetic logic unit (ALU), a storage unit, and an I/O unit, all connected by a single connection unit. The instruction fetch by the control unit alternates with operand fetches and result stores for the ALU.

Processor Architecture Jurij Silc, Borut Robic, Theo Ungerer, 1999-06-08 A survey of architectural mechanisms and implementation techniques for exploiting fine and coarse grained parallelism within microprocessors. Beginning with a review of past techniques, the monograph provides a comprehensive account of state-of-the-art techniques used in microprocessors, covering both the concepts involved and implementations in sample processors. The whole is rounded off with a thorough review of the research techniques that will lead to future microprocessors. XXXXXXXX
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Parallel and Distributed Processing and Applications Minyi Guo, 2003 This book constitutes the refereed proceedings of the International Symposium on Parallel and Distributed Processing and Applications (ISPA 2003) held in Aizu, Japan, in July 2003. The 30 revised full papers and 9 revised short papers presented together with abstracts of 4 keynotes were carefully reviewed and selected from numerous submissions. The papers are organized in topical sections on: applications on Web based and intranet systems; compiler and optimization techniques; network routing; performance evaluation of parallel systems; wireless communication and mobile computing; parallel topology; data mining and evolutionary computing; image processing and modeling; network security and database and multimedia systems.

Essentials of Computer Organization and Architecture with Navigate Advantage Access Linda Null, 2023-04-13 Essentials of Computer Organization and Architecture focuses on the function and design of the various components necessary to process

information digitally This title presents computing systems as a series of layers taking a bottom up approach by starting with low level hardware and progressing to higher level software Its focus on real world examples and practical applications encourages students to develop a big picture understanding of how essential organization and architecture concepts are applied in the computing world In addition to direct correlation with the ACM IEEE guidelines for computer organization and architecture the text exposes readers to the inner workings of a modern digital computer through an integrated presentation of fundamental concepts and principles

Improving Processor Performance and Simulation Methodology Joshua Jeffrey Yi,2003

Handbook of Signal Processing Systems Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2013-06-20 Handbook of Signal Processing Systems is organized in three parts The first part motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems the second part discusses architectures for implementing these applications the third part focuses on compilers and simulation tools describes models of computation and their associated design tools and methodologies This handbook is an essential tool for professionals in many fields and researchers of all levels

25th Euromicro Conference: Workshop on Music Technology and Audio Processing. Workshop on Dependable Computing Systems. Workshop on Software Process and Product Improvement. Workshop on Multimedia and Telecommunications. Special session on network computing ,1999

EUROMICRO 99, Proceedings of the 25th EUROMICRO Conference ,1999 Annotation This is a two volume set of the proceedings of the September 1999 conference on the current and future developments in informatics theories and application areas Volume I 80 contributions discusses digital system design architectures and methods and tools Volume II 30 contributions covers music technology and audio processing dependable computing systems software process and product improvement multimedia and telecommunication and network computing Lacks a subject index Annotation copyrighted by Book News Inc Portland OR

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Superscalar Microprocessor Design Mike Johnson,1991 The term superscalar describes a computer architecture that achieves performance by concurrent execution of scalar instructions Superscalar architectures represent the next step in the evolution of microprocessors This book is intended as a technical tutorial and introduction for engineers computer scientists The book concentrates on reduced instruction set RISC processors

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