

EIA/IPC/JEDEC J-STD-002D

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# **JOINT INDUSTRY STANDARD**

Solderability Tests for  
Component Leads,  
Terminations, Lugs,  
Terminals and Wires



# J Std 002d Solderability Tests For Component Leads

**Clyde Coombs**



## **J Std 002d Solderability Tests For Component Leads:**

*EIA/IPC/JEDEC J-STD-002D Solderability Tests for Component Leads, Terminations, Lugs, Terminals and Wires (English Language), J-STD-002D*, 2013-05      **Principles of Reliable Soldering Techniques** R. Sengupta, 1997 Soldering Though Being An Age Old Phenomenon Is Still Perhaps A Difficult Subject To Understand Due To Its Interdisciplinary Nature In This Book Efforts Have Been Made To Describe The Physical Theories Responsible For Making A Good Joint The Chemical Actions During Its Formation And The Electrical Thermal And Mechanical Requirements Essential To Ensure Its Reliability The Four M S Material Machine Method And Man Necessary For Designing A Solder Joint Have Been Described In Detail Further Process Control Solder Joint Inspection Criteria Solder Joint Defect Analysis And Its Repair Rework Are Also Discussed Additionally Brief Introductions To Surface Mount Devices Smd And Surface Mount Technology Smt Have Been Included A Annexures The Book Will Be Useful In Industry And To Design Production Process Planning And Quality Control Engineers As Well As In Engineering Technical Colleges To Students As A Reference Book For The Present And Hopefully Future Modified Courses The Academicians May Find This Book Useful For Redesigning The Present Diploma Electronics B Sc Electronics B Sc Instrumentation B E And M E M Tech Electrical Electronic Instrumentation Syllabus      **Lead-Free Soldering** Jasbir Bath, 2007-06-26 The past few years have seen major developments in soldering materials and processes for electronics assembly manufacture due to the movement from tin lead to lead free soldering The removal of lead from electronics solders due to environmental considerations first developed with proposed US legislation in the early 1990s At that time the alternatives had not been fully explored so a ban on the use of lead in electronic solders was put on hold However the seed was sown for development with various projects initiated during the 1990s in Europe the Americas and Asia Based on government pressures Japan OEMs began to move to lead free solder products from 1998 and this combined with the European Union ROHS Restriction of Hazardous Substances legislation enacted in 2006 drove the global manufacture of electronics consumer products with le free solders From 1998 to the present the development of lead free solder materials and processes has progressed to such an extent that development work moving forward will typically only concentrate on lead free solders and components rather than tin lead solders and components This book aims to give the latest information on development of the lead free soldering materials and processes and identify where more work is needed The chapters of the book describe legislation alloys reflow wave rework reliability backward and forward process compatibility PCB surface finishes and PCB laminates and standards affecting the general lead free soldering arena      *Principles of Soldering* Giles Humpston, David M. Jacobson, 2004      **Failure Modes and Mechanisms in Electronic Packages** P. Singh, Puligandla Viswanadham, 2012-12-06 Those of us who grew up in the through hole age of electronic packaging are probably more amazed and appreciative than are our children at the incredible growth of electronic performance capability My son an electrical engineering student seems almost to take for granted the innovations that leave me somewhat awestruck at times

Electronic circuit designers delight in packing more punch into less volume while reminding us that their job has become increasingly challenging. The lay person also has learned from the media that the industry has been working wonders in shrinking the transistor and expanding the power of the chip. Much attention is focussed on the silicon and on the marvelous production and entertainment tools we now see in our offices and homes. Between the silicon and the end product lies the less publicized world of circuit level packaging. We leave it to a cadre of technologists to take the schematics and parts lists and to develop the processes that turn the designers concepts into physical reality. And while the silicon transistor is shrinking the engineering challenges of packaging multiple chips and associated components into increasingly dense subsystems are growing. Further the transistor may have to function without failure through severe industrial or military environments over the lifetime of the product.

**Surface Mount Technology** Ray Prasad, 2013-11-27 A foreword is usually prepared by someone who knows the author or who knows enough to provide additional insight on the purpose of the work. When asked to write this foreword I had no problem with what I wanted to say about the work or the author. I did however wonder why people read a foreword. It is probably of value to know the background of the writer of a book. It is probably also of value to know the background of the individual who is commenting on the work. I consider myself a good friend of the author and when I was asked to write a few words I felt honored to provide my view of Ray Prasad his expertise and the contribution that he has made to our industry. This book is about the industry its technology and its struggle to learn and compete in a global market bursting with new ideas to satisfy a voracious appetite for new and innovative electronic products. I had the good fortune to be there at the beginning or almost and have witnessed the growth and excitement in the opportunities and challenges afforded the electronic industries engineering and manufacturing talents. In a few years my involvement will span half a century.

*Index of Specifications and Standards*, 2005

**Fine Pitch Surface Mount Technology** Phil Marcoux, 2013-11-27 Fine pitch high lead count integrated circuit packages represent a dramatic change from the conventional methods of assembling electronic components to a printed interconnect circuit board. To some these FPT packages appear to be an extension of the assembly technology called surface mount or SMT. Many of us who have spent a significant amount of time developing the process and design techniques for these fine pitch packages have concluded that these techniques go beyond those commonly used for SMT. In 1987 the present author convinced of the uniqueness of the assembly and design demands of these packages chaired a joint committee where the members agreed to use fine pitch technology FPT as the defining term for these demands. The committee was unique in several ways one being that it was the first time three U S standards organizations the IPC Lincolnwood IL the EIA Washington D C and the ASTM Philadelphia came together to create standards before a technology was in high demand. The term fine pitch technology and its acronym FPT have since become widely accepted in the electronics industry. The knowledge of the terms and demands of FPT currently exceed the usage of FPT packaged components but this is changing rapidly because of the size performance and

cost savings of FPT I have resisted several past invitations to write other technical texts However I feel there are important advantages and significant difficulties to be encountered with FPT *Printed Circuits Handbook* Clyde Coombs,2007-08-29 The World s 1 Guide to Printed Circuit Boards\_Now Completely Updated with the Latest Information on Lead Free Manufacturing The best reference in the field for over 30 years the Printed Circuits Handbook equips you with definitive coverage of every facet of printed circuit assemblies\_from design methods to fabrication processes Now completely revised and updated the Sixth Edition presents the latest information on lead free manufacturing including lead free PCB design and fabrication techniques lead free materials and lead free reliability models The new edition also explores best practices for High Density Interconnect HDI as well as flexible printed circuits Written by a team of experts from around the world the Sixth Edition of this renowned handbook contains cutting edge material on engineering and design of printed circuits fabrication methods assembly processes solders and soldering test and repair waste minimization and treatment quality and reliability of printed circuit processes and much more The updated Printed Circuits Handbook provides you with Unsurpassed guidance on printed circuits\_from design to manufacturing Over 500 illustrations charts and tables for quick access to essential data New to this edition New coverage of lead free PCB design and manufacturing techniques lead free materials lead free reliability models best practices for High Density Interconnect HDI and flexible printed circuits Inside This State of the Art Printed Circuits Guide Introduction to Printed Circuits Engineering and Design of Printed Circuits Fabrication Processes Assembly Processes Solders and Soldering Test and Repair Waste Minimization and Treatment Quality and Reliability of Printed Circuit Processes Flexible Circuits *Soldering Handbook* Paul Thomas Vianco,1999

**Requirements for Soldering Fluxes** Institute for Interconnecting and Packaging Electronic Circuits (Northbrook, Ill.),1995 Requirements for Electronic Grade Solder Alloys and Fluxed and Non-fluxed Solid Solders for Electronic Soldering Applications Institute for Interconnecting and Packaging Electronic Circuits (Northbrook, Ill.),1995 **Electronic Materials and Processes Handbook** Charles A. Harper,1994 Today the successful design and manufacture of electronic devices requires expertise in both materials science and manufacturing processes This reference provides electronics engineers and materials scientists with the information they need on the materials and processes currently used to fabricate interconnect and package electronic components and systems *Proceedings of the Technical Conference* ,1994

Modern Solder Technology for Competitive Electronics Manufacturing Jennie S. Hwang,1996 Introduction Advanced Surface Mount Technology and Die Attach Techniques Solder Material Soldering Chemistry Solderability Microstructure of Solders Aqueous Cleaning Manufacture No Clean Manufacture Protective and Reactive Atmosphere Soldering Surface Mount Fine Pitch Technology Surface Mount BGA PAC Technology Soldering Methodology and Equipment Soldering and Soldering Related Issues Strengthened Solders Lead Free Solders Solder Joint Failure Mode Solder Joint Failure Assessment Case Studies Solder Joint Quality and Reliability New and Emerging Specifications and Standards Future Trends **Proceedings**

**of the ... IEEE International Symposium on Electronics and the Environment ,2002      Solderability Tests for Component Leads, Terminations, Lugs, Terminals and Wires ,2008      Surface-mount Technology for PC Boards**  
Glenn R. Blackwell,James K. Hollomon,2006 Learn to generate high manufacturing yields low testing costs and reproducible designs using the latest components of surface mount technology SMT Manufacturers managers engineers students and others who work with printed circuit boards will find a wealth of cutting edge information about SMT and fine pitch technology FPT in this new edition Practical data and clear illustrations combine to clearly and accurately present the details of design for manufacturability environmental compliance design for test and quality reliability for today s miniaturized electronics packaging      **Proceedings of the Technical Program ,2002      ASM Handbook** ASM International.  
Handbook Committee,2000 This index eliminates that need to search through multiple back of the book indexes to find where a subject is addressed The A to Z listing will help users find important handbook content in volumes where they may not have thought to look

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