

Con. 6524-13.

GS-6336

(3 Hours)

[Total Marks : 100

- N.B. (1) Question No. 1 is compulsory.
 (2) Attempt any four from remaining questions.
 (3) Figures to the right indicate full marks.

1. (a) Give TTL-CMOS interfacing. 5
 (b) Compare combinational circuit with sequential circuit. 5
 (c) Compare binary code with BCD code. Give the example. 5
 (d) Compare synchronous with asynchronous counter. 5
2. (a) Using Quine McClusky technique, minimize the following function :— 10

$$F(A, B, C, D) = \sum m (3, 4, 5, 7, 9, 13, 14, 15)$$

 (b) Design a 2 bit synchronous up/down counter using T-flip flops. 10
3. (a) Implement the following Boolean function using single 4 : 1 MUX and few logic gates :— 10

$$F(A, B, C, D) = \sum m (0, 2, 4, 5, 7, 8, 9, 11, 14)$$

 (b) Compare the various logic families with respect to their characteristics. Give at least five points with their practical values. 10
4. (a) Draw and explain the working of Universal Shift Register. 10
 (b) Design a circuit to compare two 2-bit numbers. Implement this circuit using only NOR gates. 10
5. (a) Using one's and two's compliment methods perform $(52)_{10} - (18)_{10}$. 5
 (b) Using Boolean Laws prove that :— 5

$$A \bar{B} \bar{C} + A \bar{B} C + \bar{A} B C + A B C = AB + BC + CA$$

 (c) Design BCD to Excess - 3 code converter. 10
6. (a) Draw a neat circuit of master slave JK flip flop using logic gates. Write its truth table and explain its operation. 10
 (b) Draw a neat circuit of BCD adder and explain its working. 10
7. Write short notes on the following :—
 (a) One bit comparator 7
 (b) FPGA 8
 (c) Fan in and Fan out. 5

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