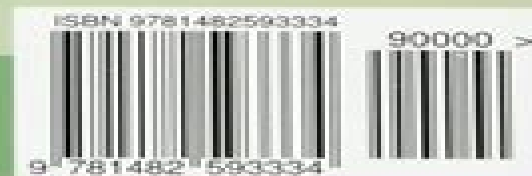


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Venktesh Shukla,  
EDA Veteran and President, TIE Silicon Valley

The book is intended for digital and system design engineers with emphasis on design and system architecture. The book is broadly divided into two sections - chapters 1 through 10, focusing on the digital design aspects and chapters 11 through 20, focusing on the system aspects of chip design. It comes with real-world examples in Verilog and introduction to SystemVerilog Assertions (SVA).



# Advanced Design Practical Examples Verilog

**Michael D. Ciletti**



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