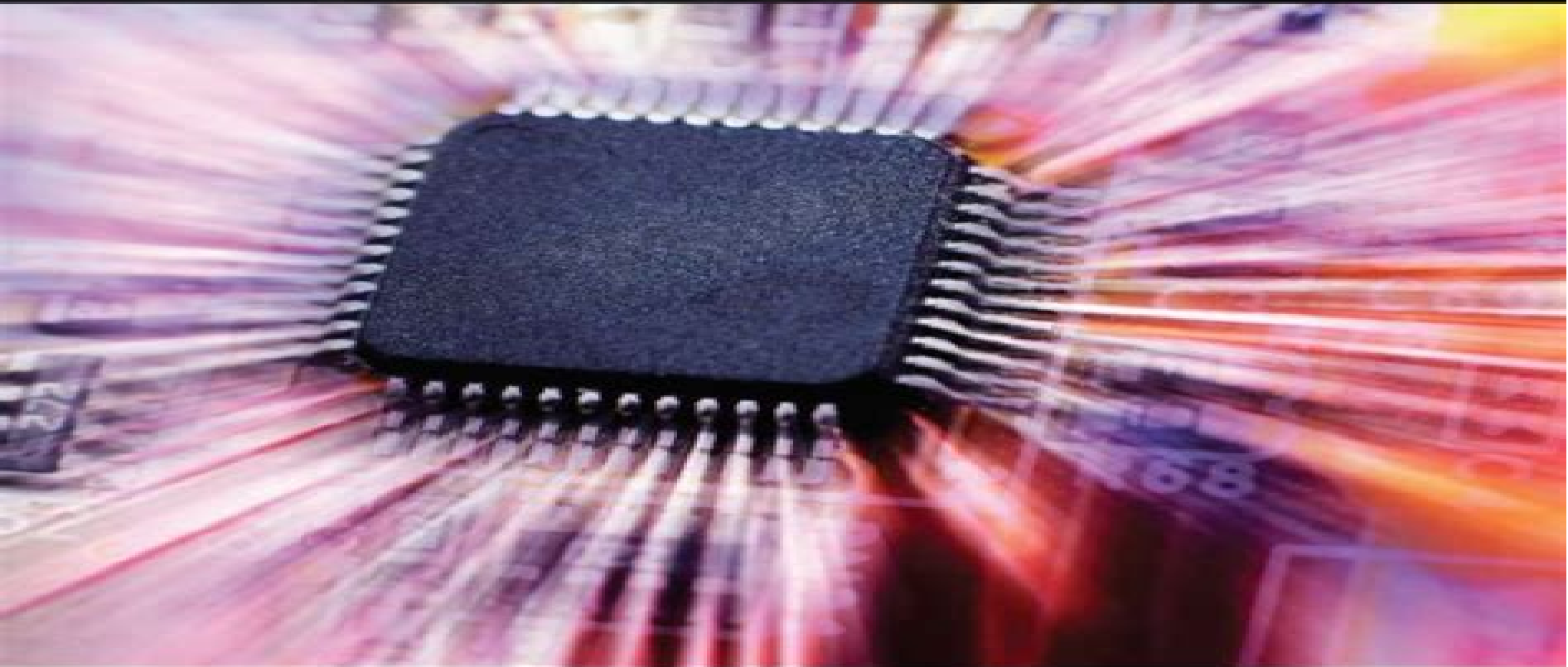


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Operation and Modeling of the MOS Transistor Yannis Tsividis, Colin McAndrew, 2011 The MOS Metal Oxide Semiconductor transistor is the most important building block of modern silicon integrated circuits This book fills an important gap in the literature by presenting a unified treatment of the operation and modeling of the MOS transistor that is complemented with extensive intuitive discussions The MOS transistor is the dominant VLSI Very Large Scale Integration device and understanding of this device is mandatory for those people planning a career in device physics and modeling as well as in circuit design Especially important for university courses there is a logical systematic and progressive description that starts with semiconductor fundamentals and builds up to a comprehensive understanding of the basics of MOS transistors For practicing professionals there are details of nuances observed in MOS transistor behavior and various approaches to modeling these are presented Detailed derivations are given for modeling dc currents charges for large signal operation small signal operation at low frequencies and high frequencies and noise

□□□□□□□□, 1990 *DIGITAL ELECTRONICS AND LOGIC DESIGN* NAIR, B. SOMANATHAN, 2002-01-01 Designed as a textbook for undergraduate students in Electrical Engineering Electronics Computer Science and Information Technology this up to date well organized study gives an exhaustive treatment of the basic principles of Digital Electronics and Logic Design It aims at bridging the gap between these two subjects The many years of teaching undergraduate and

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Semiconductor Devices and Integrated Electronics A. G. Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification amplification oscillators modulation and logic and Switching circuits The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels frequency speed size and cost are discussed The text also briefly mentions how devices are used and presents circuits and comments on representative applications Thus the book seeks a balance between the extremes of device physics and circuit design

Books in Print Supplement, 2002

Handbook of VLSI Microlithography, 2nd Edition John N. Helbert, 2001-04 This handbook gives readers a close look at the entire technology of printing very high resolution and high density integrated circuit IC patterns into thin resist process transfer coatings including optical lithography electron beam ion beam and x ray lithography The book's main theme is the special printing process needed to achieve volume high density IC chip production especially in the Dynamic Random Access Memory DRAM industry The book leads off with a comparison of various lithography methods covering the three major patterning parameters of line space resolution line edge and pattern feature dimension control The book's explanation of resist and resist process equipment technology may well be the first practical description of the relationship between the resist process and equipment parameters The basics of resist technology are completely covered including an entire chapter on resist process defectivity and the potential yield limiting effect on device production Each alternative lithographic technique and testing method is considered and evaluated basic metrology including optical scanning electron microscope SEM techniques and electrical test devices along with explanations of actual printing tools and their design construction and performance The editor devotes an entire chapter to today's sophisticated complex electron beam printers and to the emerging x ray printing technology now used in high density CMOS devices

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