

# Challenge Tutorial 01: Using Design Variables and Parametric Analysis

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## Introduction

Design variables provide a method for specifying critical parameters just before a simulation. This allows you to make quick changes to parameter values across single or multiple circuit components without having to edit any schematic components. Design variables can also be used in simple parametric analysis, which can change a design variable across a range of values and display multiple simulation results on a single plot.

## Using Design Variables

This part of the tutorial will show you how to parametrize the width of the PCBs and their placement of an inductor by using design variables. These variable values are then set using the **Running Through Environment** panel in simulation.

1. Make a copy of the circuit with components you have previously designed, or create a new one.
2. Place the inductors in the circuit.
3. Edit each one of the PCBs inductor to adjust it, and name the properties by using clicking on it and entering **Properties**. For you can give "y" as identifier.
4. Define the rest in the width box, and type  $y$ . This specifies that a variable called  $y$  will be used to set the width of the PCBs. When you do this, observe that values for "Thermal/Electrical Diffusion Rate" change from a constant value to a formula that involves the variable  $y$ . Always check to make sure that formula is updated when you change the width to a variable otherwise, your simulation results will be incorrect. Click **OK** to apply the change.
5. Repeat this procedure to change the width of the PCBs to  $y$ .
6. Name **Change  $y$  to 1.000 and name**. Verify that there are no errors.
7. Launch the **Analysis Running Through Environment**.
8. Set up the simulation environment as you normally would for simulating a schematic. This includes specifying a simulation file and transient run length, making the SPICE model reader in the environment options, and selecting the outputs to be plotted on the schematic.
9. In **ANYSYS**, select **Variables  $\rightarrow$  Copy Properties**. The design variables  $y$  and  $y$  should appear in the Design Variables area of the **Running Environment** window.
10. In **ANYSYS**, select **Variables  $\rightarrow$  Edit**. Clicked on  $y$ . In the value box, type 1.000 and then apply the value by clicking **Apply**. Change the value of  $y$  to 0.90 and click **Apply**. Check the values have been assigned, click **OK**. See Figure 1 and 2 below.
11. In **ANYSYS**, use the connection by selecting **Simulation  $\rightarrow$  Variable and Run**. After loading the the simulation to complete, the results window will pop-up. The results should be

# Cadence Tutorial D Using Design Variables And Parametric

**Clifford Lane**



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