



**UNDERSTANDING  
CONTROL SYSTEMS IN  
ELECTRICAL ENGINEERING**

# Control System Engineering

**Marco Casella**



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web gerbang logika atau logic gate adalah bagian dasar dari perancangan sistem elektronika digital untuk mengubah masukan input menjadi sinyal keluaran output yang logis sebagai hasil dari voltase atau arus gerbang logika direpresentasikan oleh angka biner atau tabel kebenaran yaitu simbol 1 dan 0

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web gerbang logika adalah penyusun elektronika digital yang setiap cara kerja rangkaian pada gerbang logika menggunakan prinsip aljabar boolean pada dasarnya dalam ilmu elektronik suatu masukan dan keluaran dibangun oleh yang namanya voltase atau arus voltase ini biasanya dihubungkan dengan sakelar

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web baca juga gerbang logika adalah rangkaian elektronika yang menggunakan sinyal digital sebagai masukan dan keluarannya apa yang membuat rangkaian disebut sebagai gerbang adalah bahwa setiap keluaran tergantung sepenuhnya pada sinyal yang diberikan pada masukan masukannya jika sinyal digital masukannya berubah maka sinyal keluarannya

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