

# Digital Systems Design Using VHDL

## 3rd Edition Roth

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### Chapter 8 Solutions

8.1 

```
function vec2int (vec5: bit_vector(4 downto 0))
  return integer is
  variable temp: integer := 0;
begin
  for i in 4 downto 0 loop
    if vec5(i) = '1' then temp := temp + 1; end if;
    if i /= 0 then temp := temp*2; end if;
  end loop;
  return temp;
end vec2int;
```

It will take 0 simulation time for the function to execute.

8.2 

```
function compC (bit_vec: bit_vector; N: integer)
  return bit_vector is
  variable comp: bit_vector(N-1 downto 0);
  variable firstone: boolean;
begin
  for i in 0 to N-1 loop
    if not firstone then
      if bit_vec(i) = '1' then
        firstone := true; end if;
        comp(i) := bit_vec(i);
      else
        comp(i) := not bit_vec(i); end if;
    end loop;
  return comp;
end compC;
```

8.3 Assuming the array has a range 0 to  $N-1$ , and integer arrays are defined with:

```
type IArr is array (natural range <>) of integer;
```

```
function LARGEST (ARR: IArr; N: integer)
  return integer is
  variable max: integer := ARR(0);
begin
  for i in 1 to N - 1 loop
    if ARR(i) > max then max := ARR(i); end if;
  end loop;
  return max;
end LARGEST;
```

8.4 

```
function GT(A, B: bit_vector; N: integer) return boolean is
```

# Digital System Design Using Vhdl Roth Solutions

**Charles H. Roth, Larry L. Kinney**



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