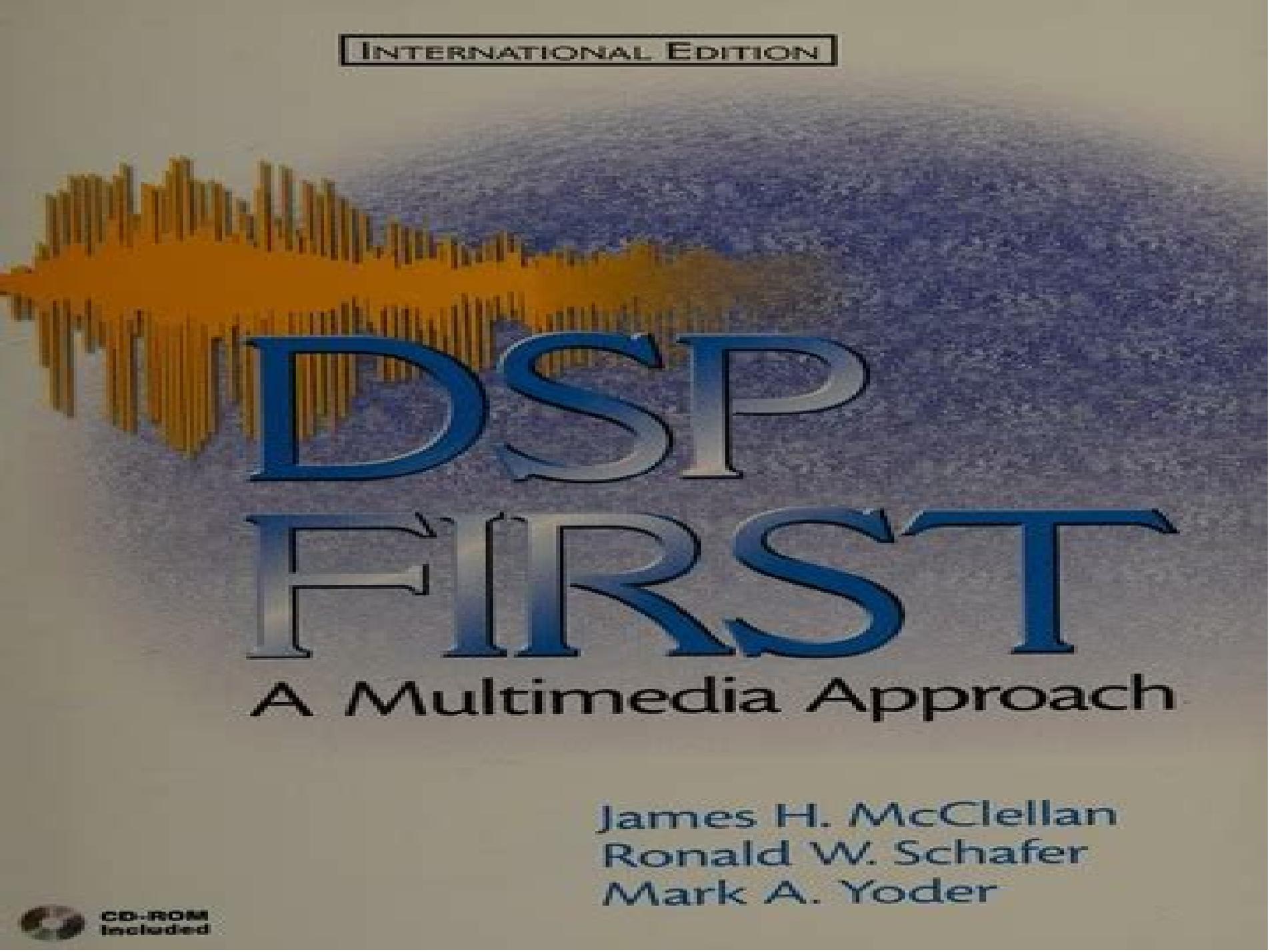


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James H. McClellan
Ronald W. Schafer
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in this volume were carefully reviewed and selected from 270 submissions They were organized in topical sections named From Perception to Action From Neurons to Networks Brain Imaging Recurrent Neural Networks Neuromorphic Hardware Brain Topology and Dynamics Neural Networks Meet Natural and Environmental Sciences Convolutional Neural Networks Games and Strategy Representation and Classification Clustering Learning from Data Streams and Time Series Image Processing and Medical Applications Advances in Machine Learning There are 63 short paper abstracts that are included in the back matter of the volume

2000 IEEE International Conference on Multimedia and Expo, ICME2000: Wednesday ,2000 Towards Network Globalization - Proceedings Of The 1991 Singapore International Conference Of Networks (Sicon '91) Gee Swee Poo,1991-08-29 With globalization in every area of human activity being a key trend of the1990s better and faster networks will have an increasingly important role and impact in making the global village a reality The papers collected in this volume highlight the global nature of the activities and the tremendous pace of R D in the field of communications and networking

1999 IEEE International Conference on Acoustics, Speech, and Signal Processing ,1999

InfoWorld ,1995-06-05 InfoWorld is targeted to Senior IT professionals Content is segmented into Channels and Topic Centers InfoWorld also celebrates people companies and projects

Proceedings ICASSP (24, 1999, Phoenix, Ariz.),1999

FPGA-based Implementation of Signal Processing Systems Roger Woods,John McAllister,Gaye Lightbody,Ying Yi,2008-10-13 Field programmable gate arrays FPGAs are an increasingly popular technology for implementing digital signal processing DSP systems By allowing designers to create circuit architectures developed for the specific applications high levels of performance can be achieved for many DSP applications providing considerable improvements over conventional microprocessor and dedicated DSP processor solutions The book addresses the key issue in this process specifically the methods and tools needed for the design optimization and implementation of DSP systems in programmable FPGA hardware It presents a review of the leading edge techniques in this field analyzing advanced DSP based design flows for both signal flow graph SFG based and dataflow based implementation system on chip SoC aspects and future trends and challenges for FPGAs The automation of the techniques for component architectural synthesis computational models and the reduction of energy consumption to help improve FPGA performance are given in detail Written from a system level design perspective and with a DSP focus the authors present many practical application examples of complex DSP implementation involving high performance computing e g matrix operations such as matrix multiplication high speed filtering including finite impulse response FIR filters and wave digital filters WDFs adaptive filtering e g recursive least squares RLS filtering transforms such as the fast Fourier transform FFT FPGA based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio telecommunication information audio visual and security applications Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing

shall also find this volume of interest **Electrical & Electronics Abstracts** ,1997 **Proceedings** ,2003 **The Application of Programmable DSPs in Mobile Communications** Alan Gatherer,Edgar Auslander,2001-12-21 With the introduction of WAP in Europe and I mode in Japan mobile terminals took their first steps out of the world of mobile telephony and into the world of mobile data At the same time the shift from 2nd generation to 3rd generation cellular technology has increased the potential data rate available to mobile users by tenfold as well as shifting data transport from circuit switched to packet data These fundamental shifts in nature and the quantity of data available to mobile users has led to an explosion in the number of applications being developed for future digital terminal devices Though these applications are diverse they share a common need for complex Digital Signal Processing DSP and in most cases benefit from the use of programmable DSPs Digital Signal Processors Features contributions from experts who discuss the implementation and applications of programmable DSPs Includes detailed introductions to speech coding speech recognition video and audio compression biometric identification and their application for mobile communications devices Discusses the alternative DSP technology which is attempting to unseat the programmable DSP from the heart of tomorrow s mobile terminals Presents innovative new applications that are waiting to be discovered in the unique environment created when mobility meets signal processing The Application of Programmable DSPs in Mobile Communications provides an excellent overview for engineers moving into the area of mobile communications or entrepreneurs looking to understand state of the art in mobile terminals It is also a must for students and professors looking for new application areas where DSP technology is being applied

Books in Print Supplement ,2002 **Embedded Microprocessor Systems** Christian Müller-Schloer,1996 Embedded microprocessor systems are affecting our daily lives at a fast pace mostly unrecognised by the general public Most of us are aware of the part they are playing in increasing business efficiency through office applications such as personal computers printers and copiers Only a few people however fully appreciate the growing role of embedded systems in telecommunications and industrial environments or even in everyday products like cars and home appliances The challenge to engineers and managers is not only highlighted by the sheer size of the market 1 5 billion microcontrollers and microprocessors are produced every year but also by the accelerating innovation in embedded systems towards higher complexity in hardware software and tools as well as towards higher performance and lower consumption To maintain competitiveness in this demanding environment an optimum mix of innovation time to market and system cost is required Choosing the right options and strategies for products and companies is crucial and rarely obvious In this book the editors have therefore skilfully brought together more than fifty contributions from some of the leading authorities in embedded systems The papers are conveniently grouped in four sections **Books In Print 2004-2005** Ed Bowker Staff,Staff Bowker, Ed,2004 **International Conference on Digital Signal Processing Proceedings** ,2002 **Byte** ,1996-05

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