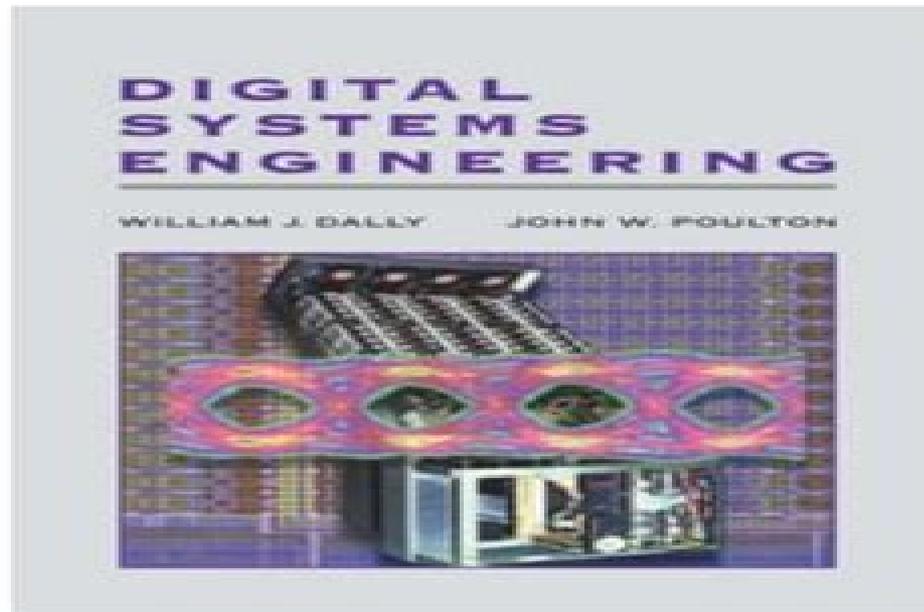


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Processors and Systems Stephen W. Keckler, Kunle Olukotun, H. Peter Hofstee, 2009-08-29 Multicore Processors and Systems provides a comprehensive overview of emerging multicore processors and systems It covers technology trends affecting multicores multicore architecture innovations multicore software innovations and case studies of state of the art commercial multicore systems A cross cutting theme of the book is the challenges associated with scaling up multicore systems to hundreds of cores The book provides an overview of significant developments in the architectures for multicore processors and systems It includes chapters on fundamental requirements for multicore systems including processing memory systems and interconnect It also includes several case studies on commercial multicore systems that have recently been developed and deployed across multiple application domains The architecture chapters focus on innovative multicore execution models as well as infrastructure for multicores including memory systems and on chip interconnections The case studies examine multicore implementations across different application domains including general purpose server media broadband network processing and signal processing Multicore Processors and Systems is the first book that focuses solely on multicore processors and systems and in particular on the unique technology implications architectures and implementations The book has contributing authors that are from both the academic and industrial communities

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Engineering DALLY, What makes some computers slow? Why do some digital systems operate reliably for years while others fail mysteriously every few hours? How can some systems dissipate kilowatts while others operate off batteries? These questions of speed, reliability and power are all determined by the system level electrical design of a digital system. Digital Systems Engineering presents a comprehensive treatment of these topics. It combines a rigorous development of the fundamental principles in each area with real world examples of circuits and methods. The book not only serves as an undergraduate textbook filling the gap between circuit design and logic design but can also help practising digital designers keep pace with the speed and power of modern integrated circuits. The techniques described in this book once used only in supercomputers are now essential to the correct and efficient operation of any type of digital system. **Wireline**

Equalization using Pulse-Width Modulation Johan Hendrik Rutger Schrader, 2007 Source-Synchronous Networks-On-Chip Ayan Mandal, Sunil P. Khatri, Rabi Mahapatra, 2013-11-19 This book describes novel methods for network on chip NoC design using source synchronous high speed resonant clocks. The authors discuss NoCs from the bottom up providing circuit level details before providing architectural simulations. As a result readers will get a complete picture of how a NoC can be designed and optimized. Using the methods described in this book readers are enabled to design NoCs that are 5X better than existing approaches in terms of latency and throughput and can also sustain a significantly greater amount of traffic. Advanced Signal Integrity for High-Speed Digital Designs Stephen H. Hall, Howard L. Heck, 2011-09-20 A synergistic approach to signal integrity for high speed digital design. This book is designed to provide contemporary

readers with an understanding of the emerging high speed signal integrity issues that are creating roadblocks in digital design Written by the foremost experts on the subject it leverages concepts and techniques from non related fields such as applied physics and microwave engineering and applies them to high speed digital design creating the optimal combination between theory and practical applications Following an introduction to the importance of signal integrity chapter coverage includes Electromagnetic fundamentals for signal integrity Transmission line fundamentals Crosstalk Non ideal conductor models including surface roughness and frequency dependent inductance Frequency dependent properties of dielectrics Differential signaling Mathematical requirements of physical channels S parameters for digital engineers Non ideal return paths and via resonance I O circuits and models Equalization Modeling and budgeting of timing jitter and noise System analysis using response surface modeling Each chapter includes many figures and numerous examples to help readers relate the concepts to everyday design and concludes with problems for readers to test their understanding of the material Advanced Signal Integrity for High Speed Digital Designs is suitable as a textbook for graduate level courses on signal integrity for programs taught in industry for professional engineers and as a reference for the high speed digital designer

On-Chip Networks Natalie Enright, Li-shiuan Peh, 2022-11-10 With the ability to integrate a large number of cores on a single chip research into on chip networks to facilitate communication becomes increasingly important On chip networks seek to provide a scalable and high bandwidth communication substrate for multi core and many core architectures High bandwidth and low latency within the on chip network must be achieved while fitting within tight area and power budgets In this lecture we examine various fundamental aspects of on chip network design and provide the reader with an overview of the current state of the art research in this field Table of Contents Introduction Interface with System Architecture Topology Routing Flow Control Router Microarchitecture Conclusions On-Chip Networks Natalie Enright Jerger, Tushar Krishna, Li-Shiuan Peh, 2017-06-19 This book targets engineers and researchers familiar with basic computer architecture concepts who are interested in learning about on chip networks This work is designed to be a short synthesis of the most critical concepts in on chip network design It is a resource for both understanding on chip network basics and for providing an overview of state of the art research in on chip networks We believe that an overview that teaches both fundamental concepts and highlights state of the art designs will be of great value to both graduate students and industry engineers While not an exhaustive text we hope to illuminate fundamental concepts for the reader as well as identify trends and gaps in on chip network research With the rapid advances in this field we felt it was timely to update and review the state of the art in this second edition We introduce two new chapters at the end of the book We have updated the latest research of the past years throughout the book and also expanded our coverage of fundamental concepts to include several research ideas that have now made their way into products and in our opinion should be textbook concepts that all on chip network practitioners should know For example these fundamental concepts include message passing multicast routing and bubble flow control

schemes Digital Design William James Dally,R. Curtis Harting,2012-09-17 This book provides students with a system level perspective and the tools they need to understand analyze and design complete digital systems using Verilog It goes beyond the design of simple combinational and sequential modules to show how such modules are used to build complete systems reflecting digital design in the real world **Integrated Circuit and System Design** ,2005 **Energy and Water Development Appropriations for 2004** United States. Congress. House. Committee on Appropriations. Subcommittee on Energy and Water Development,2003 *Electronics Now* ,1999 **108-1 Hearings: Energy And Water Development Appropriations For 2004, Part 6, March 20, 2003, *** ,2004 **Flow Control and Micro-architectural Mechanisms for Extending the Performance of Interconnection Networks** Li-Shiuan Peh,2001 **Cumulated Index to the Books** ,1999 *Parallel Computing Technologies* ,2001 IEEE Transactions on Circuits and Systems ,2006

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