

Code No: 54023

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**  
**B.Tech II Year II Semester Examinations, July/August - 2021**  
**STRUCTURED DIGITAL SYSTEM DESIGN**  
**(Electronics and Instrumentation Engineering)**

Time: 3 hours

Max. Marks: 75

Answer any five questions  
All questions carry equal marks

- 1.a) Design a BCD adder circuit. [7+8]
- b) Design a 4-bit magnitude comparator circuit using above BCD adder. [7+8]
- 2.a) Explain the terms fan-out, fan-in.
- b) What is the worst case propagation delay of given network shown in figure 1?  $A_{t1} = 10 \text{ nSec}$ ,  $A_{t2} = 20 \text{ nSec}$ ,  $A_{t3} = 25 \text{ nSec}$ ,  $A_{t4} = 10 \text{ nSec}$ . [7+8]



Figure: 1

- 3.a) Differentiate Moore machine from mealy machine. [7+8]
- b) Convert an edge triggered D - Flip flop to an edge triggered JK flip flop. [7+8]
- 4. Using the state diagram make state assignment for the given state figure 2. [15]

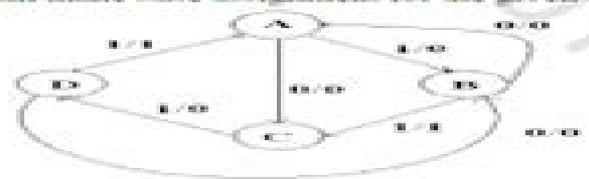


Figure: 2

- 5. Design a module - 6 Gray code counter. Make your code reflective with respect to the 2<sup>nd</sup> MSB. [15]
- 6. Design a controller that controls the drive circuit of stepper motor. Assume required control word format. [15]
- 7.a) Design a binary multiplier using parallel adder and shift register. [9+6]
- b) Explain the concept of state assignment with examples. [9+6]
- 8. Write a short note on:
  - a) FPLA architecture.
  - b) Configurable memories. [7+8]

# Digital System Design Solved Question Papers

**Rachel S Tattersall**



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## **Table of Contents Digital System Design Solved Question Papers**

1. Understanding the eBook Digital System Design Solved Question Papers
  - The Rise of Digital Reading Digital System Design Solved Question Papers
  - Advantages of eBooks Over Traditional Books
2. Identifying Digital System Design Solved Question Papers
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Digital System Design Solved Question Papers
  - User-Friendly Interface
4. Exploring eBook Recommendations from Digital System Design Solved Question Papers
  - Personalized Recommendations
  - Digital System Design Solved Question Papers User Reviews and Ratings
  - Digital System Design Solved Question Papers and Bestseller Lists
5. Accessing Digital System Design Solved Question Papers Free and Paid eBooks
  - Digital System Design Solved Question Papers Public Domain eBooks

- Digital System Design Solved Question Papers eBook Subscription Services
- Digital System Design Solved Question Papers Budget-Friendly Options
- 6. Navigating Digital System Design Solved Question Papers eBook Formats
  - ePub, PDF, MOBI, and More
  - Digital System Design Solved Question Papers Compatibility with Devices
  - Digital System Design Solved Question Papers Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Digital System Design Solved Question Papers
  - Highlighting and Note-Taking Digital System Design Solved Question Papers
  - Interactive Elements Digital System Design Solved Question Papers
- 8. Staying Engaged with Digital System Design Solved Question Papers
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Digital System Design Solved Question Papers
- 9. Balancing eBooks and Physical Books Digital System Design Solved Question Papers
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Digital System Design Solved Question Papers
- 10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Digital System Design Solved Question Papers
  - Setting Reading Goals Digital System Design Solved Question Papers
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Digital System Design Solved Question Papers
  - Fact-Checking eBook Content of Digital System Design Solved Question Papers
  - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks

### 14. Embracing eBook Trends

- Integration of Multimedia Elements
- Interactive and Gamified eBooks

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web es war ein schock der weltweit die menschen erschütterte am 11 september 2001 wurden in den usa bei vier terroranschlägen fast 3 000 menschen getötet die bilder

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web stockwerk ein ab diesem zeitpunkt waren fernsehteam vor ort die bilder aus manhattan live in die gesamte welt übertrugen nur 17 minuten später schlug der united airlines

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