

# COMPUTER ORGANIZATION AND DESIGN

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Designing Modern Embedded Systems: Software, Hardware, and Applications Stefan Henkler, Márcio Kreutz, Marco A. Wehrmeister, Marcelo Götz, Achim Rettberg, 2023-06-10 This book constitutes the refereed proceedings of the 7th IFIP TC 10 International Embedded Systems Symposium IESS 2022 held in Lippstadt Germany during November 3-4 2022 The 10 full revised papers and 2 short papers presented were carefully reviewed and selected from 13 submissions The presented research and technical works cover system level design methods algorithms verification and validation techniques estimation of system properties and characteristics performance analysis and real time systems design Also the book presents industrial and real world application case studies that discuss the challenges and realizations of modern embedded systems especially when it comes to including artificial intelligence algorithms and techniques in embedded systems

**Information Technology in Business** Amir Manzoor, 2012-03-19 Today's business is technology driven Information

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**Fundamentals of Computer Organization and Architecture** Mostafa Abd-El-Barr, Hesham El-Rewini, 2005-02-08. This is the first book in the two-volume set offering comprehensive coverage of the field of computer organization and architecture. This book provides complete coverage of the subjects pertaining to introductory courses in computer organization and architecture, including instruction set architecture and design, assembly language programming, computer arithmetic, processing unit design, memory system design, input/output design, and organization, pipelining design techniques, reduced instruction set computers (RISCs). The authors, who share over 15 years of undergraduate and graduate-level instruction in computer architecture, provide real-world applications, examples of machines, case studies, and practical experiences in each chapter.

**System-Level Design of GPU-Based Embedded Systems** Arian Maghazeh, 2018-12-07. Modern embedded systems deploy several hardware accelerators in a heterogeneous manner to deliver high-performance computing. Among such devices, graphics processing units (GPUs) have earned a prominent position by virtue of their immense computing power. However, a system design that relies on the sheer throughput of GPUs is often incapable of satisfying the strict power and time-related constraints faced by the embedded systems. This thesis presents several system-level software techniques to optimize the design of GPU-based embedded systems under various graphics and non-graphics applications. As compared to the conventional application-level optimizations, the system-wide view of our proposed techniques brings about several advantages. First, it allows for fully incorporating the limitations and requirements of the various system parts in the design process. Second, it can unveil optimization opportunities through exposing the information flow between the processing components. Third, the techniques are generally applicable to a wide range of applications with similar characteristics. In addition, multiple system-level techniques can be combined together or with application-level techniques to further improve the performance. We begin by studying some of the unique attributes of GPU-based embedded systems and discussing several factors that distinguish the design of these systems from that of the conventional high-end GPU-based systems. We then proceed to develop two techniques that address an important challenge in the design of GPU-based embedded systems from different perspectives. The challenge arises from the fact that GPUs require a large amount of workload to be present at runtime in order to deliver a high throughput. However, for some embedded applications, collecting large batches of input data requires an unacceptable waiting time, prompting a trade-off between throughput and latency. We also develop an

optimization technique for GPU based applications to address the memory bottleneck issue by utilizing the GPU L2 cache to shorten data access time Moreover in the area of graphics applications and in particular with a focus on mobile games we propose a power management scheme to reduce the GPU power consumption by dynamically adjusting the display resolution while considering the user's visual perception at various resolutions We also discuss the collective impact of the proposed techniques in tackling the design challenges of emerging complex systems The proposed techniques are assessed by real life experimentations on GPU based hardware platforms which demonstrate the superior performance of our approaches as compared to the state of the art techniques

**Computer Organization, Design, and Architecture, Fourth Edition** Sajjan G. Shiva, 2007-11-30 Suitable for a one or two semester undergraduate or beginning graduate course in computer science and computer engineering Computer Organization Design and Architecture Fourth Edition presents the operating principles capabilities and limitations of digital computers to enable development of complex yet efficient systems With 40% updated material and four new chapters this edition takes students through a solid up to date exploration of single and multiple processor systems embedded architectures and performance evaluation New to the Fourth Edition Additional material that covers the ACM IEEE computer science and engineering curricula More coverage on computer organization embedded systems networks and performance evaluation Expanded discussions of RISC CISC VLIW and parallel pipelined architectures The latest information on integrated circuit technologies and devices memory hierarchy and storage Updated examples references and problems Supplying appendices with relevant details of integrated circuits reprinted from vendors manuals this book provides all of the necessary information to program and design a computer system

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**Computer Fundamentals** B. Ram, 2000 Computer Systems Design and Architecture Vincent P. Heuring, Harry Frederick Jordan, 1997 This text serves as an introduction to and a survey of the common commercial architectures It was created with a strong electrical and computer engineering perspective including current topics such as pipelined processor design memory hierarchy and in

*Operating Systems* William Stallings, 1998 **Introduction to VLSI Circuits and Systems** John P. Uyemura, 2002 CD ROM contains AIM SPICE from AIM Software Micro Cap 6 from Spectrum Software Silos III Verilog Simulator from Simucad Adobe Acrobat Reader 4.0 from Adobe

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Digital Systems Frederick J. Hill, Gerald R. Peterson, 1987-06-19 This heavily revised edition of the well received text teaches the organization and design of complex digital systems Unique in its effective exploitation of a hardware description language this book s systematic approach involves the reader in the design process Rather than just describe an end product the authors employ a register transfer and control sequence design language A Hardware Program Language AHPL through which the design of a wide variety of digital hardware systems is explored Text contains much new material as well as sections on peripherals interfacing microprocessors hardware compiling processes and virtual memory hardware Includes extensive examples and problems sets

**Minicomputer and Microprocessor Interfacing** John Charles Cluley, 1982 *Indian National Bibliography*, 2008-07

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