

4-bit Synchronous Counter (74LS161)

```
// 4-bit counter

module counter_4bit(clk, nclr, nload, ent, enp, rco, count, parallel_in) :

    input  clk ;
    input  nclr ;
    input  nload ;
    input  ent ;
    input  enp ;
    output rco ;
    output [3:0] count ;
    input  [3:0] parallel_in ;

    reg    [3:0] count ;
    reg    rco ;

    always @ (count or ent)
    if ((count == 15) && (ent == 1'b1))
        rco = 1'b1 ;
    else
        rco = 1'b0 ;

    always @ (posedge clk or negedge nclr)
    begin
        if (nclr == 1'b0)
            count <= 4'd0 ;
        else if (nload == 1'b0)
            count <= parallel_in ;
        else if ((ent == 1'b1) && (enp == 1'b1))
            count <= (count + 1) & 16 ;
    end
endmodule
```

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4 Bit Counter Verilog Code Davefc:

Unveiling the Power of Verbal Beauty: An Psychological Sojourn through **4 Bit Counter Verilog Code Davefc**

In a world inundated with monitors and the cacophony of instant communication, the profound energy and psychological resonance of verbal artistry frequently fade into obscurity, eclipsed by the constant barrage of noise and distractions. However, situated within the musical pages of **4 Bit Counter Verilog Code Davefc**, a captivating perform of literary splendor that pulses with fresh emotions, lies an memorable journey waiting to be embarked upon. Written by a virtuoso wordsmith, this magical opus books visitors on a psychological odyssey, softly revealing the latent potential and profound impact embedded within the complicated internet of language. Within the heart-wrenching expanse of the evocative examination, we will embark upon an introspective exploration of the book is central themes, dissect its captivating publishing design, and immerse ourselves in the indelible impact it leaves upon the depths of readers souls.

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