

Computer Architecture

A Quantitative Approach

计算机体系结构 量化研究方法

(英文版·第2版)

(美) David A. Patterson 著
John L. Hennessy 著

Computer Architecture A Quantitative Approach 2nd Edition

Laurence T. Yang, Minyi Guo



Computer Architecture A Quantitative Approach 2nd Edition:

Computer Architecture John L. Hennessy, David A. Patterson, 2011-10-07 *Computer Architecture A Quantitative Approach* Fifth Edition explores the ways that software and technology in the cloud are accessed by digital media such as cell phones, computers, tablets, and other mobile devices. The book, which became a part of Intel's 2012 recommended reading list for developers, covers the revolution of mobile computing. It also highlights the two most important factors in architecture today: parallelism and memory hierarchy. This fully updated edition is comprised of six chapters that follow a consistent framework: explanation of the ideas in each chapter, a crosscutting issues section which presents how the concepts covered in one chapter connect with those given in other chapters, a putting it all together section that links these concepts by discussing how they are applied in real machine and detailed examples of misunderstandings and architectural traps commonly encountered by developers and architects. Formulas for energy, static and dynamic power, integrated circuit costs, reliability, and availability are included. The book also covers virtual machines, SRAM and DRAM technologies, and new material on Flash memory. Other topics include the exploitation of instruction level parallelism in high performance processors, superscalar execution, dynamic scheduling, and multithreading, vector architectures, multicore processors, and warehouse scale computers (WSCs). There are updated case studies and completely new exercises. Additional reference appendices are available online. This book will be a valuable reference for computer architects, programmers, application developers, compiler and system software developers, computer system designers, and application developers. Part of Intel's 2012 Recommended Reading List for Developers. Updated to cover the mobile computing revolution. Emphasizes the two most important topics in architecture today: memory hierarchy and parallelism in all its forms. Develops common themes throughout each chapter: power, performance, cost, dependability, protection, programming models, and emerging trends. What's Next. Includes three review appendices in the printed text. Additional reference appendices are available online. Includes updated Case Studies and completely new exercises.

Computer Architecture John L. Hennessy, David A. Patterson, Christos Kozyrakis, 2025-12-02 *Computer Architecture A Quantitative Approach* has been considered essential reading by instructors, students, and practitioners of computer design for nearly 30 years. The seventh edition of this classic textbook from John Hennessy and David Patterson, winner of a 2019 Textbook Excellence Award (Texty) from the Textbook and Academic Authors Association, follows a consistent framework: explanation of the ideas in each chapter, a crosscutting issues section which presents how the concepts covered in one chapter connect with those given in other chapters, a putting it all together section that links these concepts by discussing how they are applied in real machine and detailed examples of misunderstandings and architectural traps commonly encountered by developers and architects. Includes Putting It All Together sections near the end of every chapter providing real world technology examples that demonstrate the principles covered in each chapter. Covers new developments in GPU and CPU architectures as well as domain specific architectures. Features more

comprehensive coverage of systems on chip and heterogeneity **Computer Architecture** John L. Hennessy, David A. Patterson, 2003 *Performance Evaluation of Complex Systems: Techniques and Tools* Maria Carla Calzarossa, Salvatore Tucci, 2003-08-02 This book presents the tutorial lectures given by leading experts in the area at the IFIP WG 7.3 International Symposium on Computer Modeling Measurement and Evaluation Performance 2002 held in Rome Italy in September 2002 The survey papers presented are devoted to theoretical and methodological advances in performance and reliability evaluation as well as new perspectives in the major application fields Modeling and verification issues solution methods workload characterization and benchmarking are addressed from the methodological point of view Among the applications dealt with are hardware and software architectures wired and wireless networks grid environments Web services and real time voice and video processing This book is intended to serve as a state of the art survey and reference for students scientists and engineers active in the area of performance and reliability evaluation Web Engineering and Peer-to-Peer Computing Enrico Gregori, Ludmila Cherkasova, Gianpaolo Cugola, Fabio Panzneri, Gian P. Picco, 2003-06-30 This book constitutes the refereed proceedings of the two thematic workshops held jointly with Networking 2002 WEB Engineering and Peer to Peer Computing Networking 2002 was organized by the Italian National Research Council CNR and was sponsored by the IFIP working groups WG 6.2 Network and Intern work Architectures WG 6.3 Performance of Communication Systems and WG 6.8 Wireless Communications The program of the conference covered ve days and included the main conference three days two tutorial days and one day of thematic workshops

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High-Performance Computing Laurence T. Yang,Minyi Guo,2006 With hyperthreading in Intel processors hypertransport links in next generation AMD processors multi core silicon in today s high end microprocessors from IBM and emerging grid computing parallel and distributed computers have moved into the mainstream

Optimizing Inter-instruction Value Communication Through Degree of Use Prediction Jeffrey Adam Butts,2004

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