

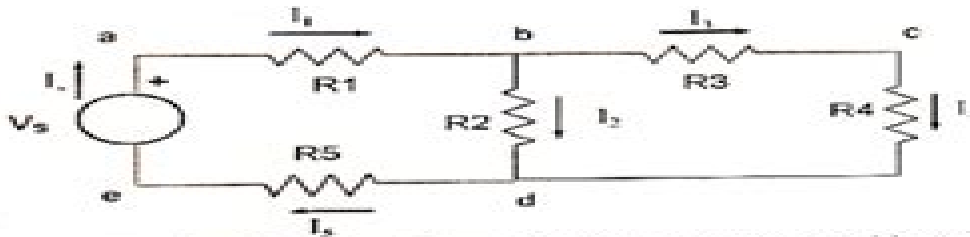
EXPERIMENT No.1

Aim: To verify Kirchhoff Voltage Law and Kirchhoff Current Law using hardware and Proteus software.

Apparatus required:

| S. No. | Items | Specifications | Quantity |
|--------|-------------------|---------------------|----------|
| 1 | DC Voltage source | 0-20V | 01 |
| 2 | Resistors | 1-2 ohms | 05 |
| 3 | Ammeter | Digital type | 01 |
| 4 | Voltmeter | Digital type | 01 |
| 5 | Connecting wires | As per requirements | |

- Kirchhoff's Voltage Law states that the algebraic sum of all the voltages around any closed path (loop or mesh) is zero.



Applying Kirchhoff's voltage law to the first and the second loops in the circuit shown in Figure yields:

$$\text{Loop 1: } -V_s + V_1 + V_2 + V_5 = 0$$

(1a)

$$\text{Loop 2: } -V_2 + V_3 + V_4 = 0$$

(1b)

- Kirchhoff's Current Law states that the algebraic sum of all the currents at any node is zero.

Applying Kirchhoff's current law to the first four nodes in the circuit shown in Figure yields the following equations;

$$\text{Node a: } -I_s + I_1 = 0$$

(2a)

$$\text{Node b: } -I_1 + I_2 + I_3 = 0$$

(2b)

$$\text{Node c: } -I_3 + I_4 = 0$$

(2c)

$$\text{Node d: } -I_2 - I_4 + I_5 = 0$$

(2d)

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