

**B.Sc. (Pass Course) 1st Semester Examination,
February-2022**

COMPUTER SCIENCE

Paper-1.2

Computer Architecture (2012-13)

Time allowed : 3 hours]

[Maximum marks : 40

Note : Attempt five questions in all, selecting one question from each unit. Question No. 1 is compulsory.

1. Write short note on the following :

निम्नलिखित पर संक्षिप्त टिप्पणी लिखिए :

(a) ~~Simplifying logic circuits ... sum of product.~~

तर्क सर्किट को सरल बनाना उत्पाद का योग।

(b) Multiplexers

मल्टीप्लेक्सर्स

(c) JK and D flip-flop with truth table

सत्य तालिका के साथ JK और D फ्लिप-फ्लॉप

(d) Micro-instructions

माइक्रो-निर्देश

Computer Architecture Exam Paper

EW Minium



Computer Architecture Exam Paper:

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Architecture Exam Review David Kent Ballast,1992

Computer Architecture MCQs Arshad Iqbal,2019-06-14 Computer Architecture Multiple Choice Questions and Answers MCQs Computer architecture quiz questions and answers with practice tests for online exam prep and job interview prep Computer architecture study guide with questions and answers about assessing computer performance computer architecture and organization computer arithmetic computer language and instructions computer memory review computer technology data level parallelism and GPU architecture embedded systems exploiting memory instruction level parallelism instruction set principles interconnection networks memory hierarchy design networks storage and peripherals pipe lining in computer architecture pipe lining performance processor datapath and control quantitative design and analysis request level and data level parallelism storage systems thread level parallelism Computer architecture trivia questions and answers to get prepare for career placement tests and job interview prep with answers key Practice exam questions and answers about computer science composed from computer architecture textbooks on chapters Assessing Computer Performance Practice Test 13 MCQs Computer Architecture and Organization Practice Test 19 MCQs Computer Arithmetic Practice Test 33 MCQs Computer Language and Instructions Practice Test 52 MCQs Computer Memory Review Practice Test 66 MCQs Computer Technology Practice Test 14 MCQs Data Level Parallelism and GPU Architecture Practice Test 38 MCQs Embedded Systems Practice Test 21 MCQs Exploiting Memory Practice Test 29 MCQs Instruction Level Parallelism Practice Test 52 MCQs Instruction Set Principles Practice Test 30 MCQs Interconnection Networks Practice Test 56 MCQs Memory Hierarchy Design Practice Test 37 MCQs Networks Storage and Peripherals Practice Test 20 MCQs Pipelining in Computer Architecture Practice Test 56 MCQs Pipelining Performance Practice Test 15 MCQs Processor Datapath and Control Practice

Test 21 MCQs Quantitative Design and Analysis Practice Test 49 MCQs Request Level and Data Level Parallelism Practice Test 32 MCQs Storage Systems Practice Test 43 MCQs Thread Level Parallelism Practice Test 37 MCQs Computer architecture interview questions and answers on 32 bits MIPS addressing addition and subtraction advanced branch prediction advanced techniques and speculation architectural design vectors architecture and networks arrays and pointers basic cache optimization methods basic compiler techniques cache optimization techniques cache performance optimizations caches and cache types caches performance case study sanyo vpc sx500 camera Computer architecture test questions and answers on cloud computing compiler optimization computer architecture computer architecture memory hierarchy computer code computer hardware operands computer hardware operations computer hardware procedures computer instructions and languages computer instructions representations computer networking computer organization computer systems virtual memory computer types cost trends and analysis Computer architecture exam questions and answers on CPU performance datapath design dependability design of memory hierarchies designing and evaluating an i o system disk storage and dependability distributed shared memory and coherence division calculations dynamic scheduling algorithm dynamic scheduling and data hazards embedded multiprocessors encoding an instruction set exceptions exploiting ilp using multiple issue fallacies and pitfalls floating point google warehouse scale GPU architecture issues Computer architecture objective questions and answers on GPU computing graphics processing units hardware based speculation how virtual memory works i o performance

Auditing & systems : exam questions and explanations Irvin N. Gleim, William A. Hillison, 2005

Architecture Exam Review: Nonstructural topics Steven E. O'Hara, David Kent Ballast, 2002 The Architect Registration Exam ARE is part of the licensing requirements for U S and Canadian architects A computerized closed book exam the ARE is administered year round at a network of test centers The topics represented on the ARE may be roughly divided into two areas structural and nonstructural We offer two primary study guides for the exam one volume devoted to each area Each volume includes concise reviews of the exam topics with practice problems and solutions Additional practice for the ARE is provided by Architecture Exam Review Site Planning and Building Design Graphic Divisions Volume II Nonstructural Topics provides a thorough review of the ARE nonstructural exam topics including the exam s most challenging divisions Site Planning and Building Design More than 80 sample questions are provided with solutions along with a valuable checklist of items that should be considered in every design solution The book also covers test taking strategy and is enhanced by illustrations figures and tables along with a detailed index The fifth edition was updated to reflect changes in the exam the adoption of a computerized format and use of the 2000 versions of the International Building Code and the International Plumbing Code

Architecture Exam Review Steven E. O'Hara, David Kent Ballast, 2005

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