

# HND Level 100 - Computer Architecture

## Structured Questions and Answers

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### Section A: Internal Structure and Operation of a Microprocessor (20 Marks)

1. a. Define the term microprocessor and list two of its major functions. (4 marks)

Answer:

A microprocessor is an integrated circuit that performs arithmetic, logic, control, and input/output operations of a computer system. It acts as the brain of a computer.

Functions:

- Performs arithmetic and logical operations
- Controls other components of the computer system

b. Describe the internal structure of a basic microprocessor with a labeled diagram. (6 marks)

Answer:

The internal structure typically includes:

- Arithmetic Logic Unit (ALU)
- Control Unit
- Registers
- Buses (data, address, control)

[Include labeled diagram manually in class or on whiteboard]

c. Explain the function of the following microprocessor components: (6 marks)

i. ALU

Performs arithmetic and logic operations.

ii. Control Unit

Directs the operations of the processor.

iii. Registers

Temporary storage locations for instructions and data.

d. Outline the fetch-decode-execute cycle. (4 marks)

Answer:

- Fetch: Retrieve instruction from memory.
- Decode: Interpret the instruction.
- Execute: Carry out the instruction.

### Section B: Computer Memory (20 Marks)

2. a. Define computer memory and distinguish between primary and secondary memory. (4 marks)

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**Fifth Generation Computer Systems** T. Moto-Oka,2012-12-02 The Japan Information Processing Development Centre JIPDEC established a committee for Study and Research on Fifth Generation Computers Beginning in 1979 this Committee set out on a two year investigation into the most desirable types of computer systems for application in the 1990 s fifth generation computers and how the development

projects aimed at the realization of these systems should be carried forward This book contains the papers presented at the International Conference on Fifth Generation Computer Systems Included among these papers is a preliminary report on the findings of the Committee

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**Japanese Technological Advances and Possible United States Responses Using Research Joint Ventures** United States. Congress. House. Committee on Science and Technology. Subcommittee on Investigations and Oversight,1984

**MCSE Designing a Microsoft Windows 2000 Directory Services Infrastructure Readiness Review; Exam 70-219** Jill Spealman,2001

**Proceedings of the Third IEEE Conference on Fuzzy Systems** ,1994

**Documentation Abstracts** ,1991

**CompTIA Security+ All-in-One Exam Guide, Fifth Edition (Exam SY0-501)** Wm. Arthur Conklin,Greg White,Dwayne Williams,Chuck Cothren,Roger L. Davis,2018-01-05 This fully updated study guide covers every topic on the current version of the CompTIA Security exam Take the latest version of the CompTIA Security exam with complete confidence using the detailed information contained in this highly effective self study system Written by a team of leading information security experts this authoritative guide addresses the skills required for securing a network and managing risk and enables you to become CompTIA Security certified CompTIA Security All in One Exam Guide Fifth Edition Exam SY0 501 covers all exam domains and features 200 accurate practice questions To aid in study the book features learning objectives at the beginning of each chapter exam tips practice exam questions and in depth explanations All questions mirror those on the live test in style format and difficulty Beyond fully preparing you for the challenging SY0 501 exam the book also serves as a valuable on the job reference for IT professionals Provides 100% coverage of every objective on exam SY0 501 Electronic content includes 200 practice questions and a secured book PDF Written by a team of experienced IT security academics

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